

FIG. 1

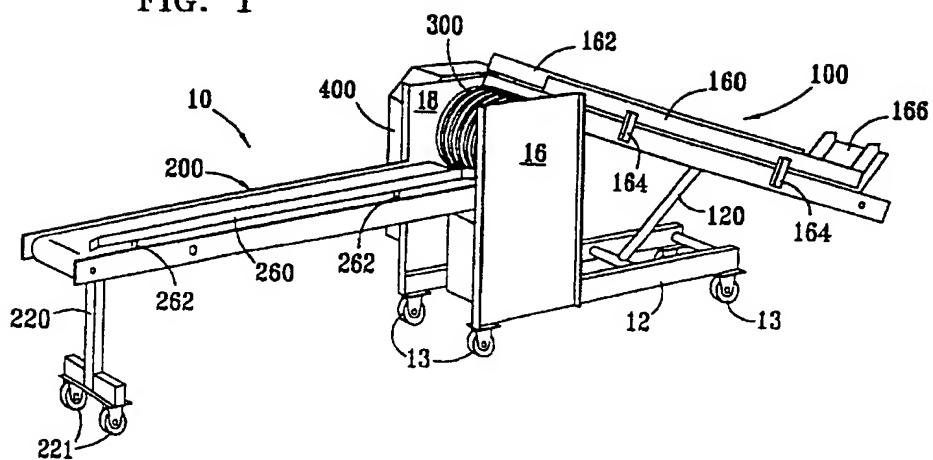


FIG. 2

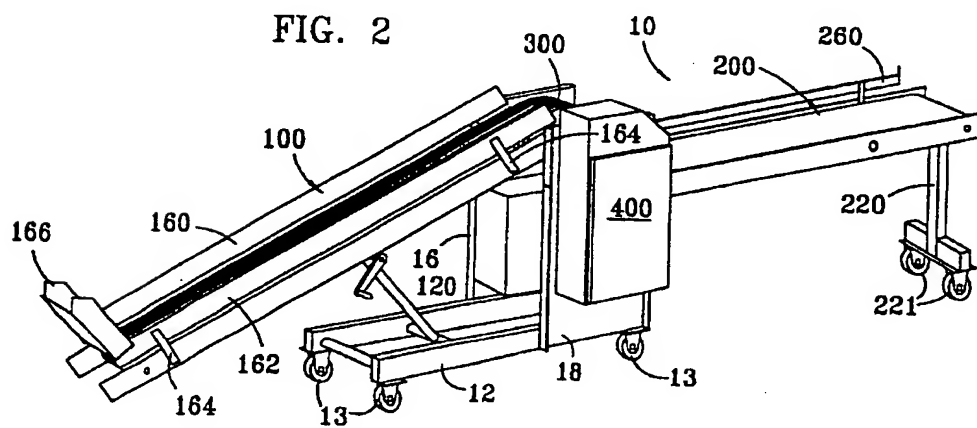


FIG. 3

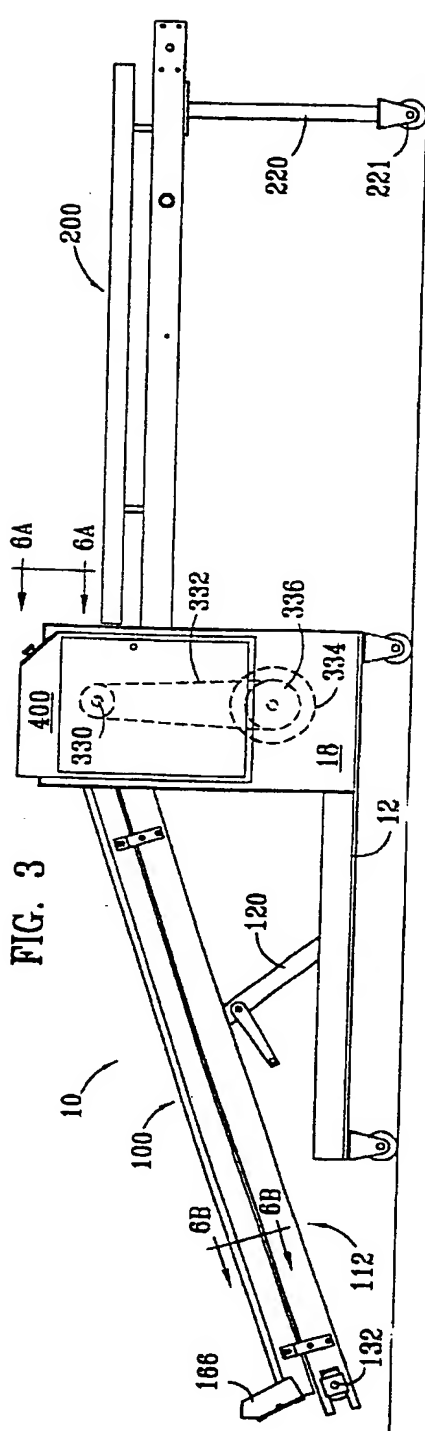


FIG. 6A

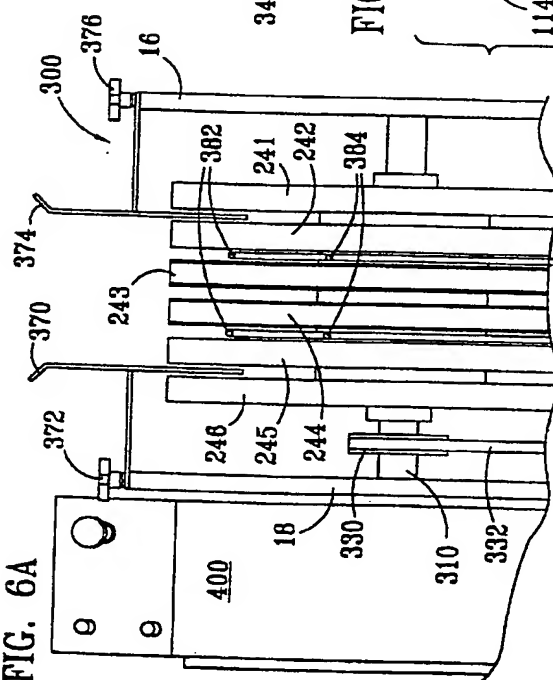
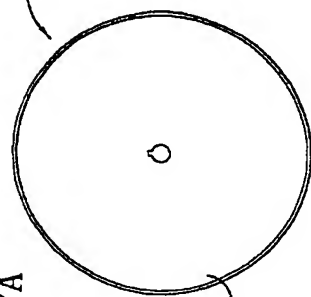


FIG. 7A



**FIG. 7B**  
144,142,145,146

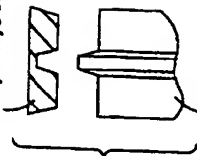


FIG. 6B

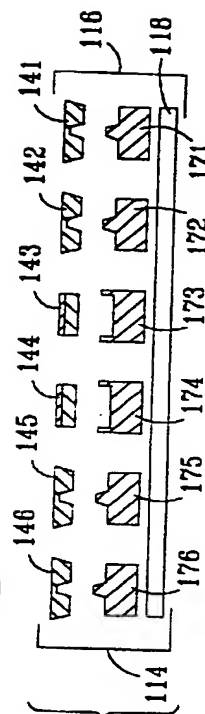


FIG. 4

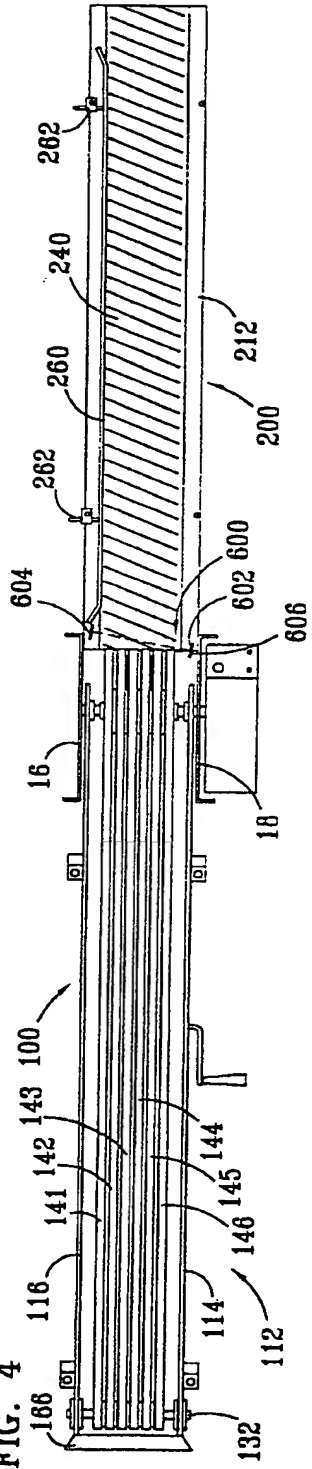
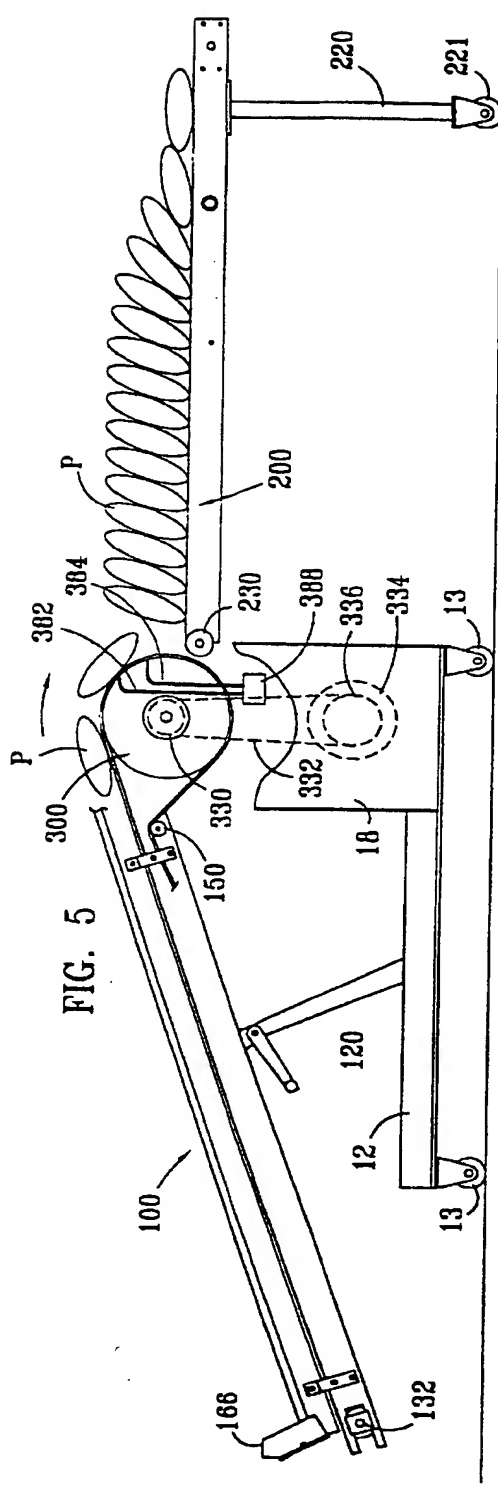


FIG. 5



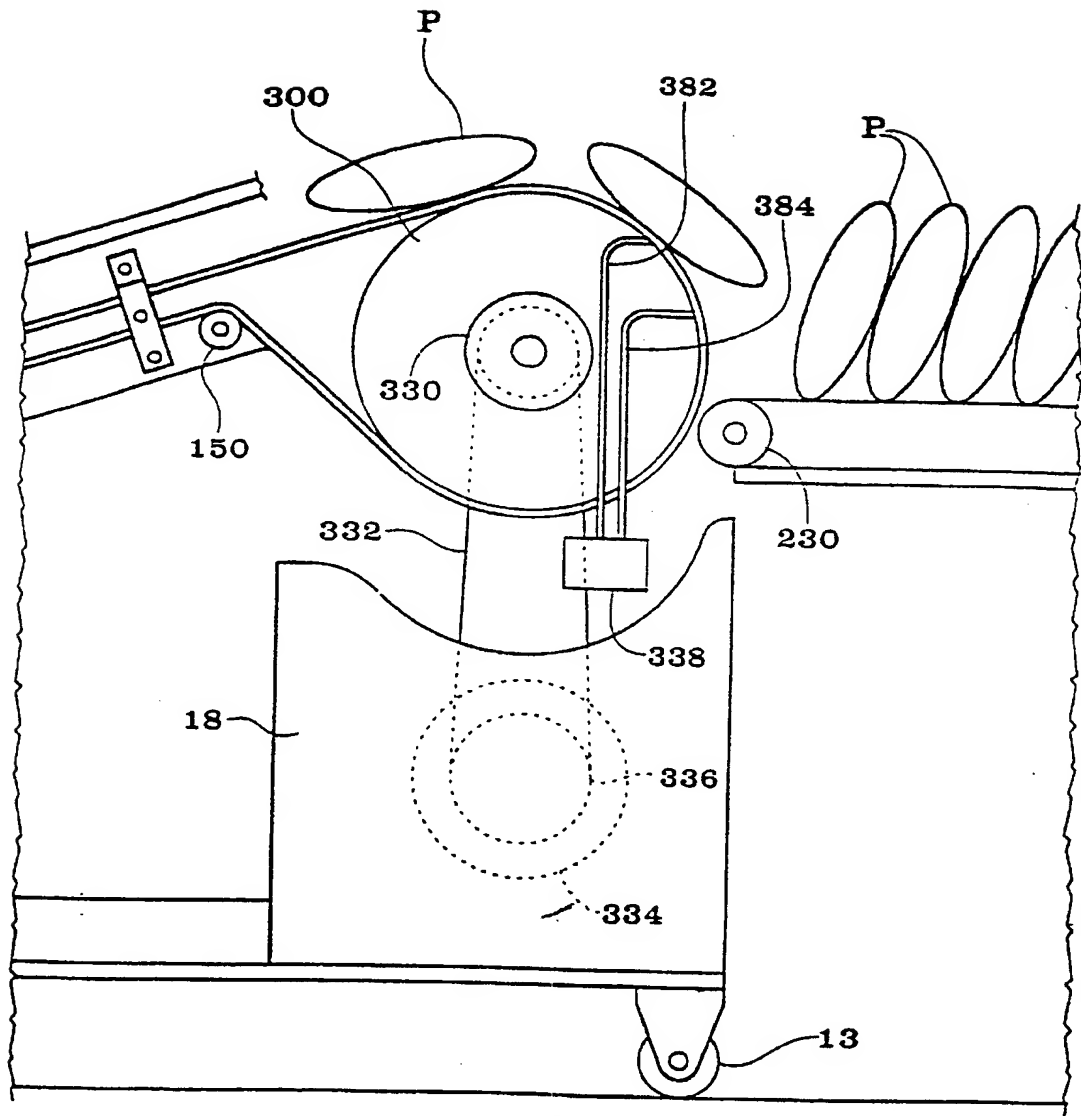


FIG.5A

FIG. 7C

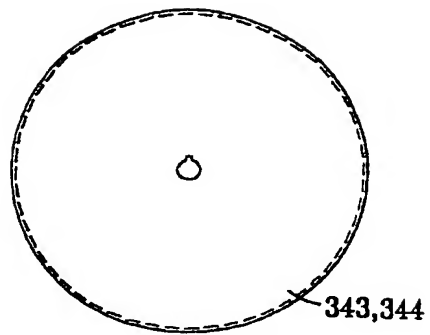


FIG. 7D

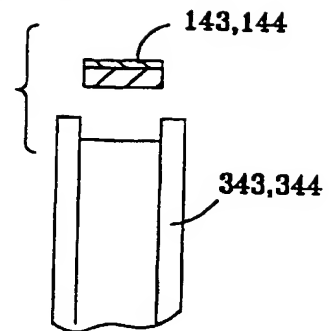


FIG. 8A

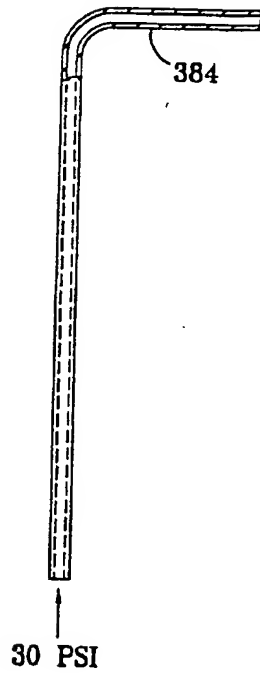


FIG. 8B

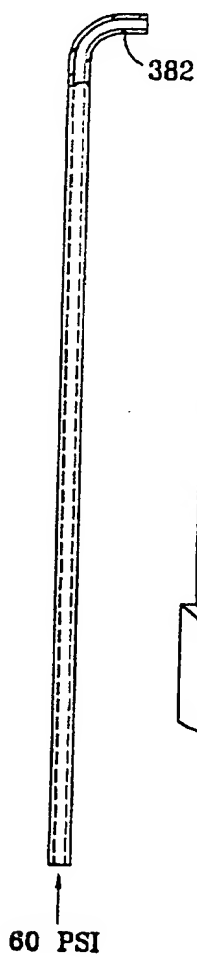


FIG. 9

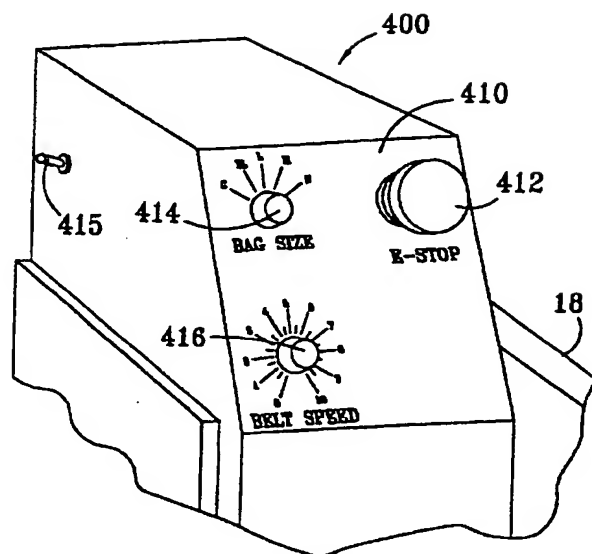


FIG. 10

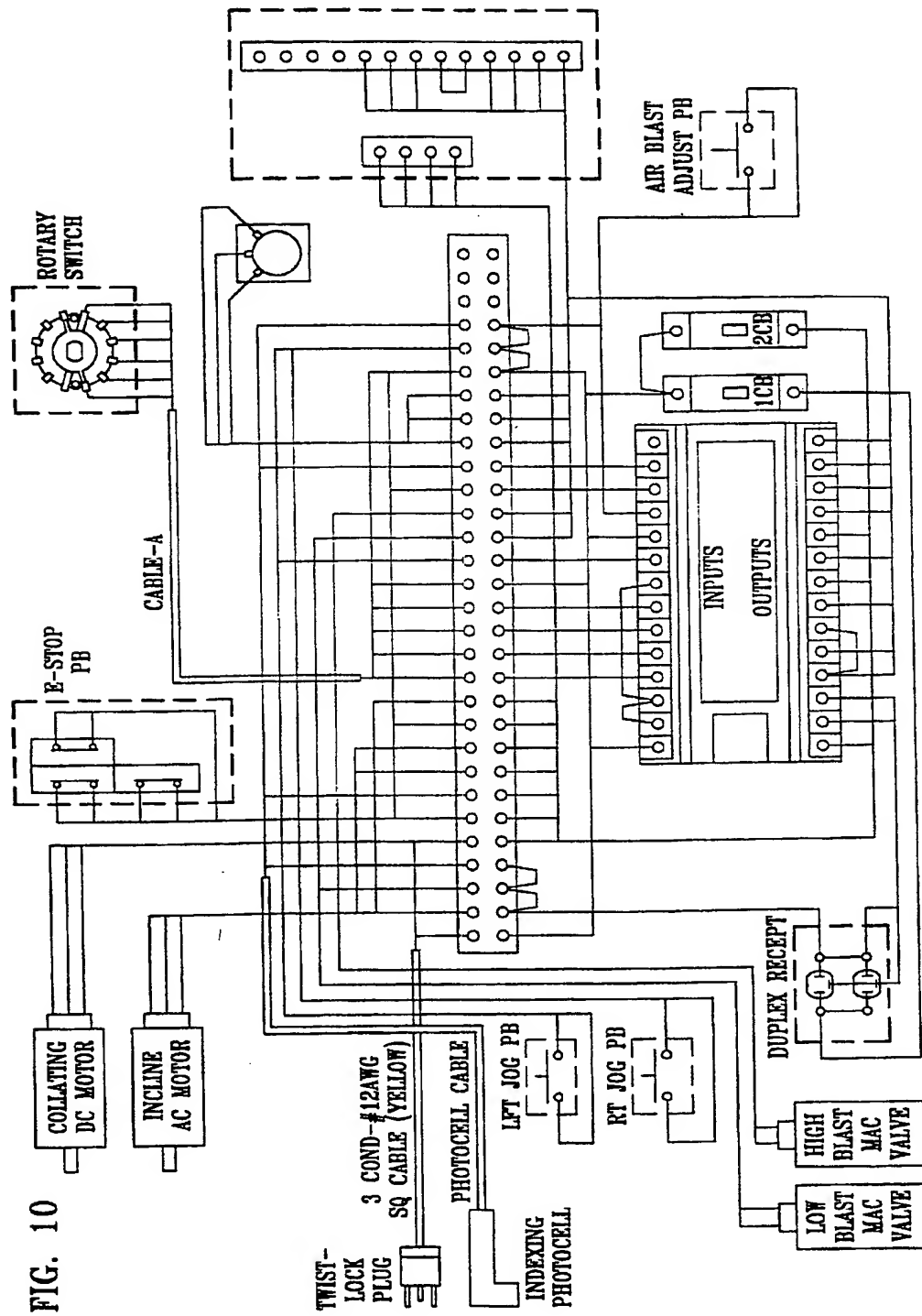


FIG. 11A

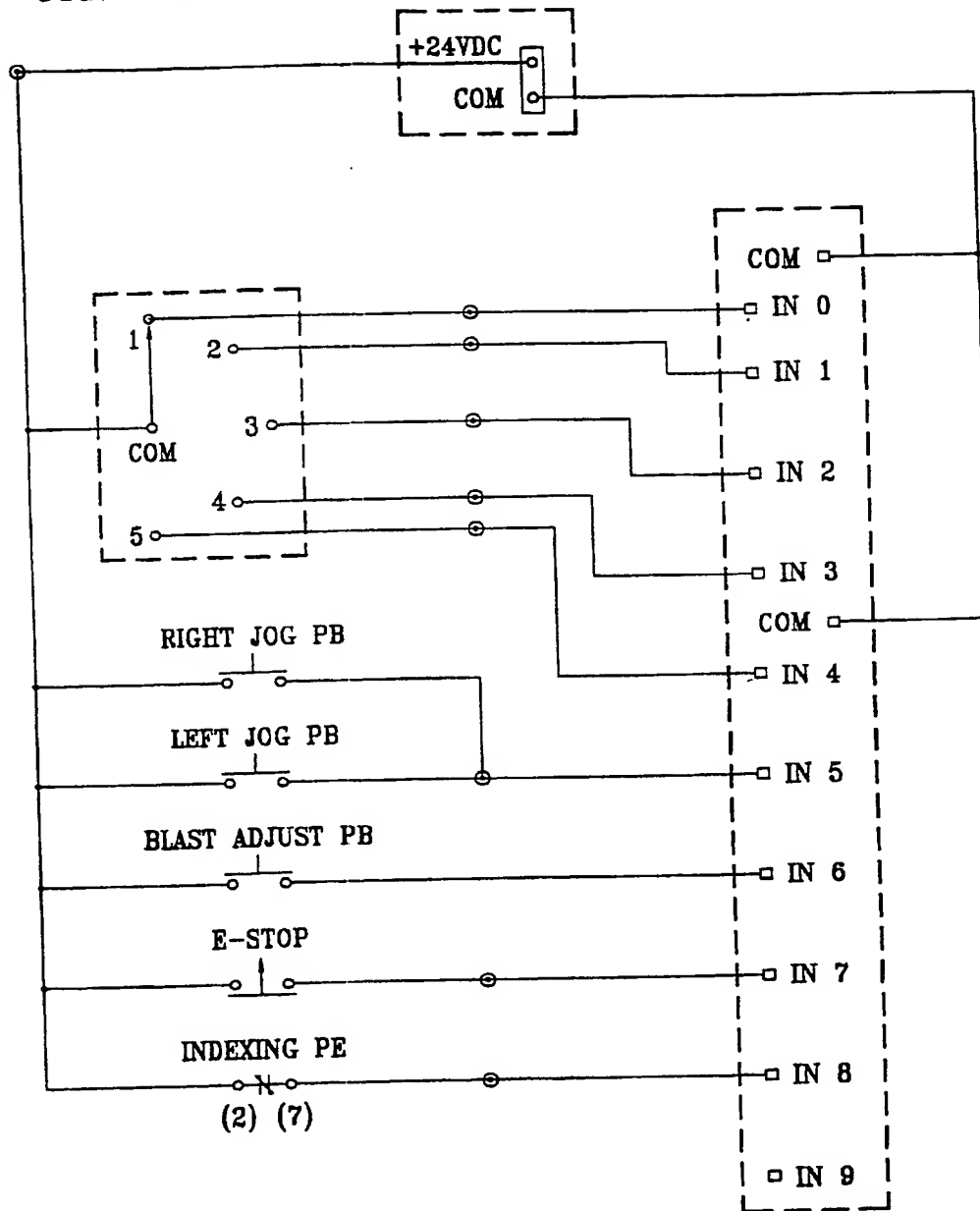
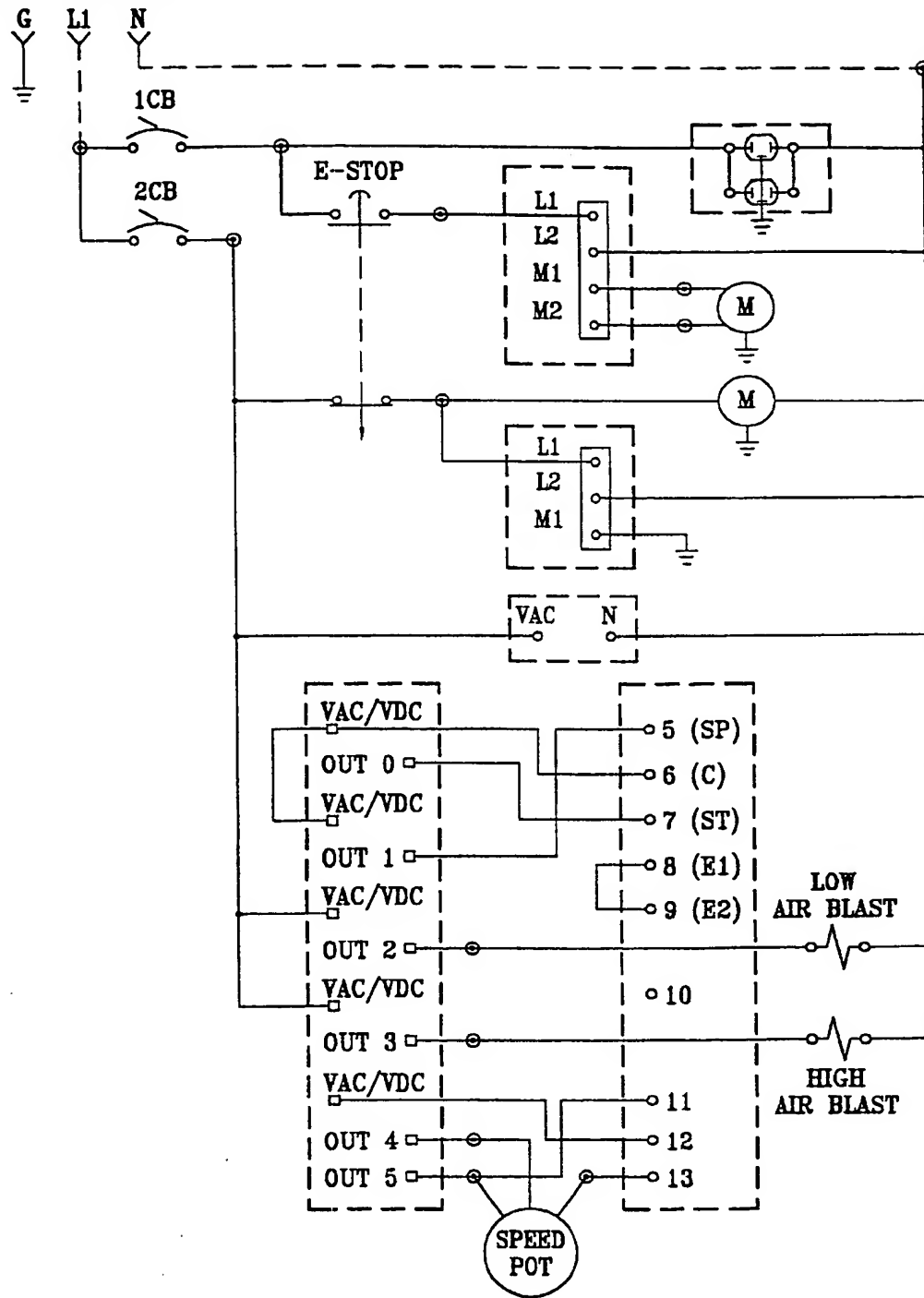


FIG. 11B

**120V, 1PH, 60Hz**





# AIR ASSISTED COLLATOR PROGRAM LISTING

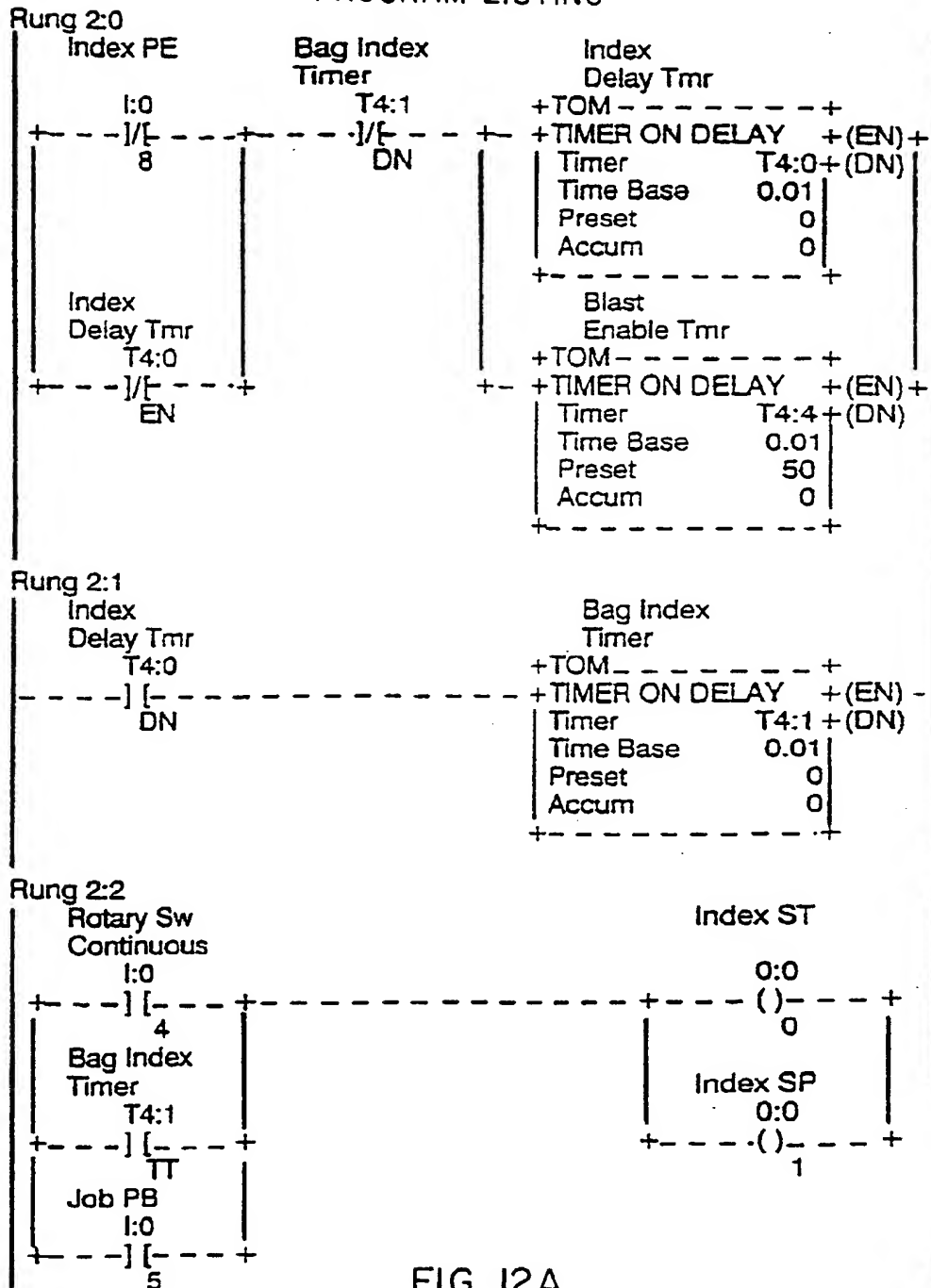


FIG. 12A

# AIR ASSISTED COLLATOR PROGRAM LISTING

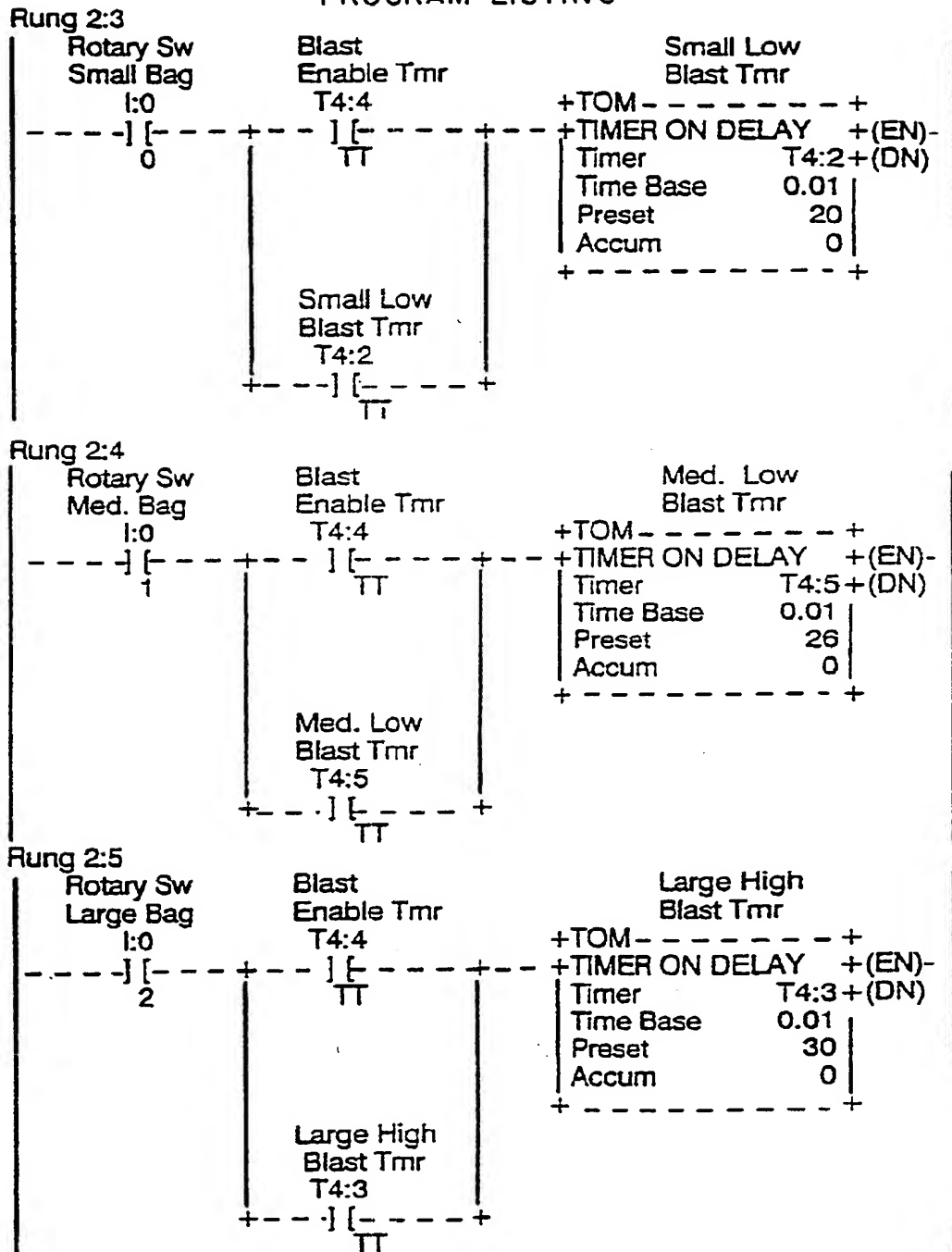
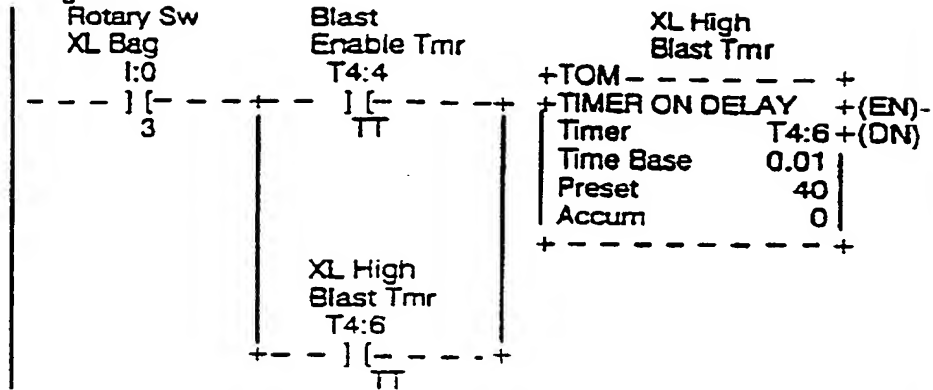


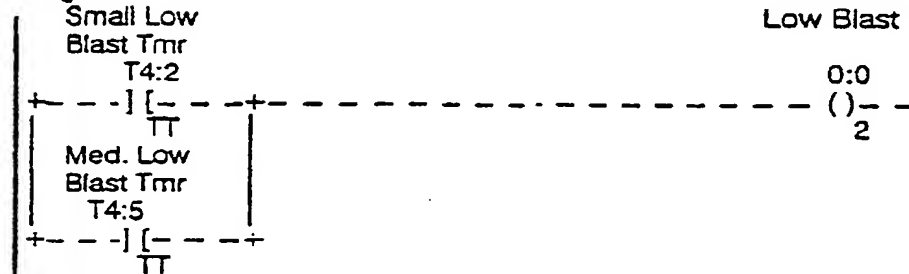
FIG. I2B

# AIR ASSISTED COLLATOR PROGRAM LISTING

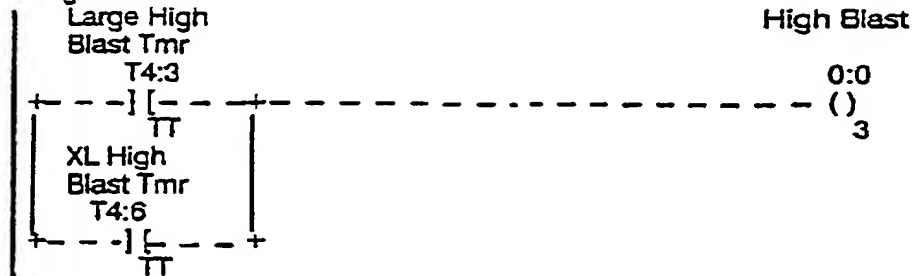
Rung 2:6



Rung 2:7



Rung 2:8



Rung 2:9

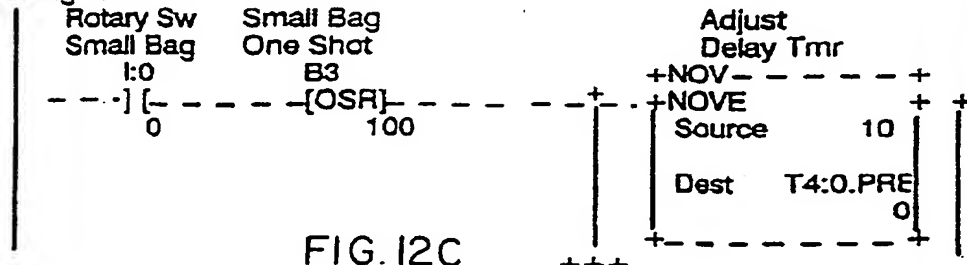


FIG. 12C

# AIR ASSISTED COLLATOR PROGRAM LISTING

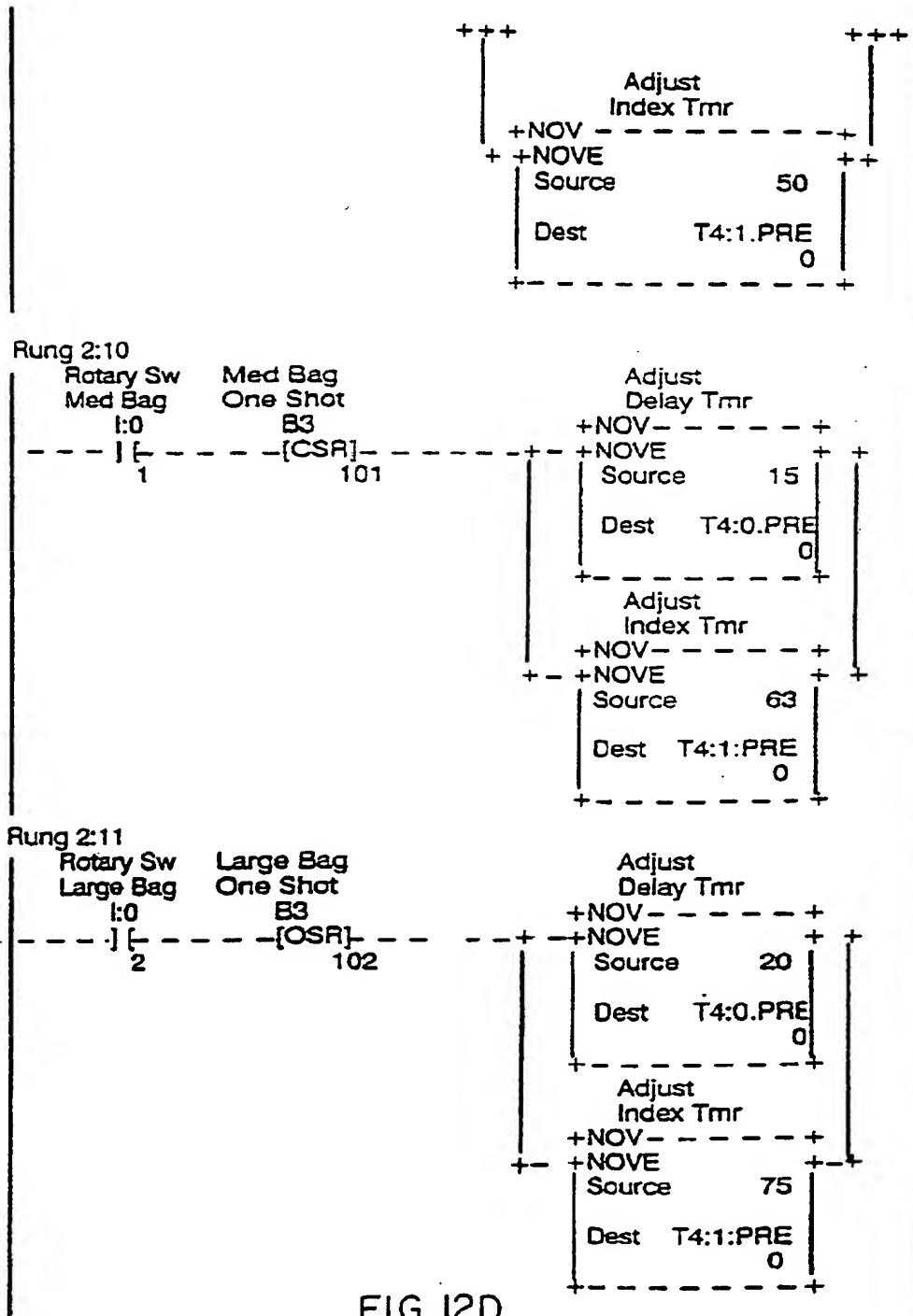
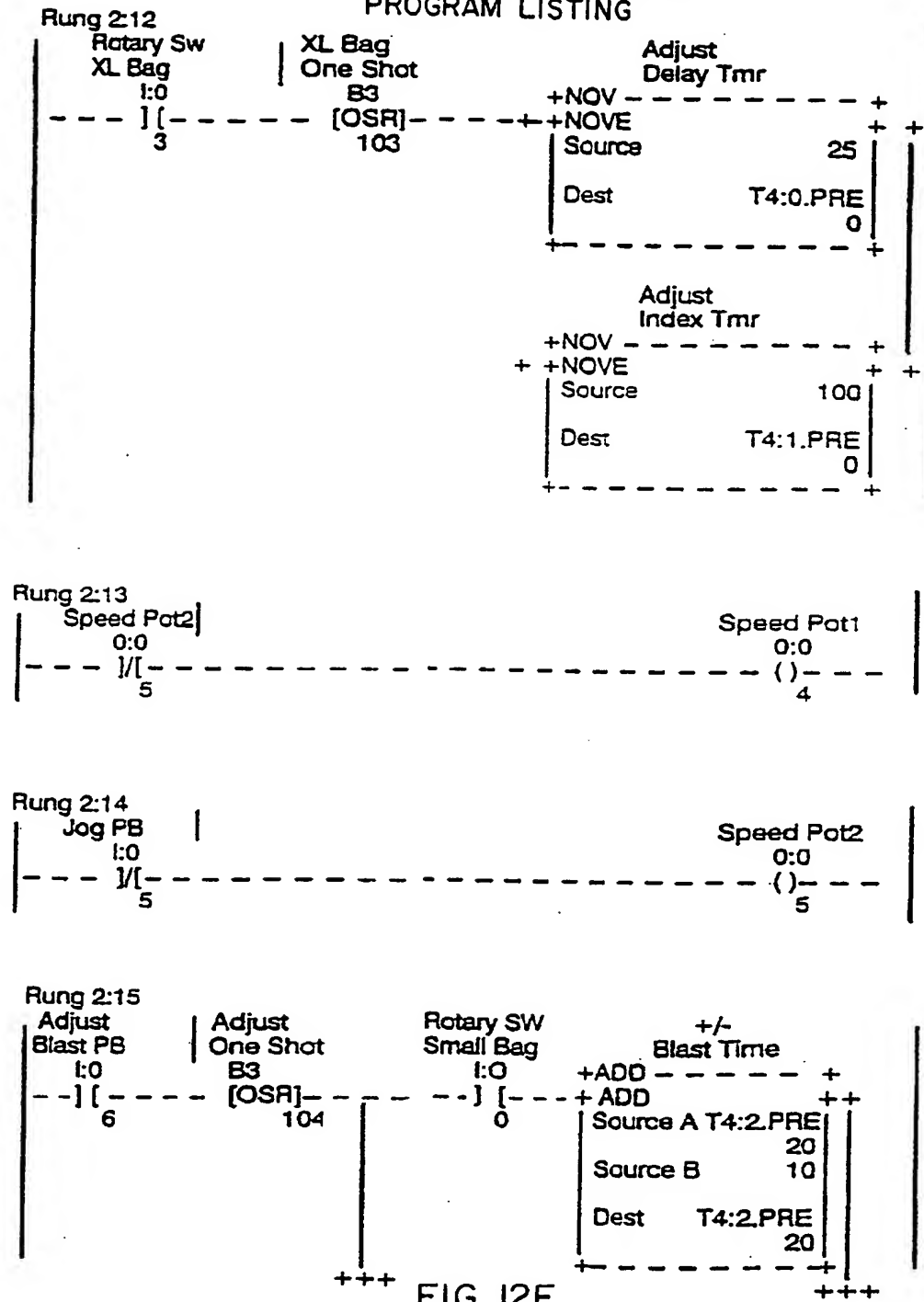
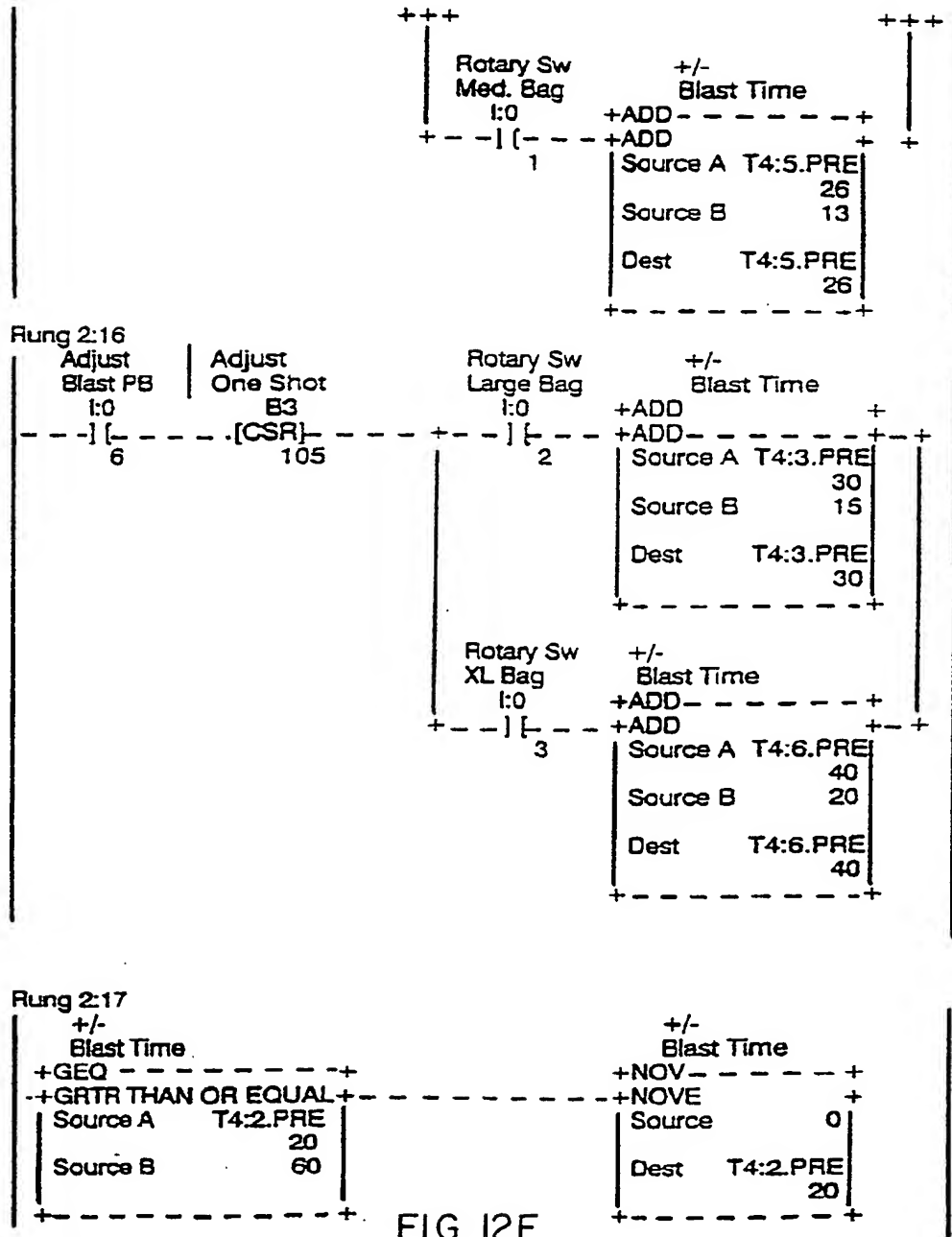


FIG. I2D

# AIR ASSISTED COLLATOR PROGRAM LISTING



# AIR ASSISTED COLLATOR PROGRAM LISTING



# AIR ASSISTED COLLATOR PROGRAM LISTING

Rung 2:18

+/- Blast Time		+/- Blast Time	
+GEQ	-----+	+NOV	-----+
+GRTR THAN OR EQUAL	+-----+	+NOVE	+-----+
Source A	T4:5.PRE	Source	0
	26		
Source B	78	Dest	T4:5.PRE
			26
+-----+		+-----+	

Rung 2:19

+/- Blast Time		+/- Blast Time	
+GEQ	-----+	+NOV	-----+
+GRTR THAN OR EQUAL	+-----+	+NOVE	+-----+
Source A	T4:3.PRE	Source	0
	30		
Source B	90	Dest	T4:3.PRE
			30
+-----+		+-----+	

Rung 2:20

+/- Blast Time		+/- Blast Time	
+GEQ	-----+	+NOV	-----+
+GRTR THAN OR EQUAL	+-----+	+NOVE	+-----+
Source A	T4:6.PRE	Source	0
	40		
Source B	120	Dest	T4:6.PRE
			40
+-----+		+-----+	

Rung 2:21

+-----+			
+END+			
+-----+			

FIG. 12G

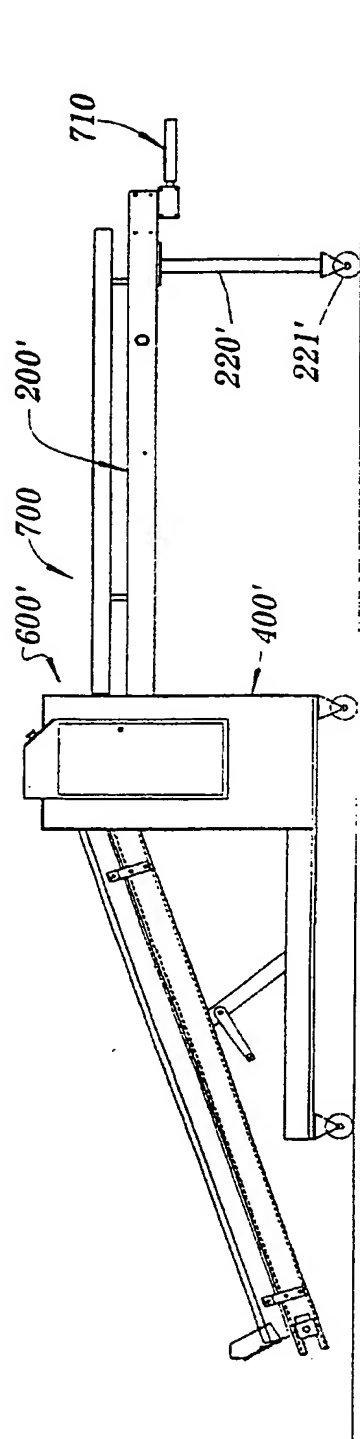


Fig. 13

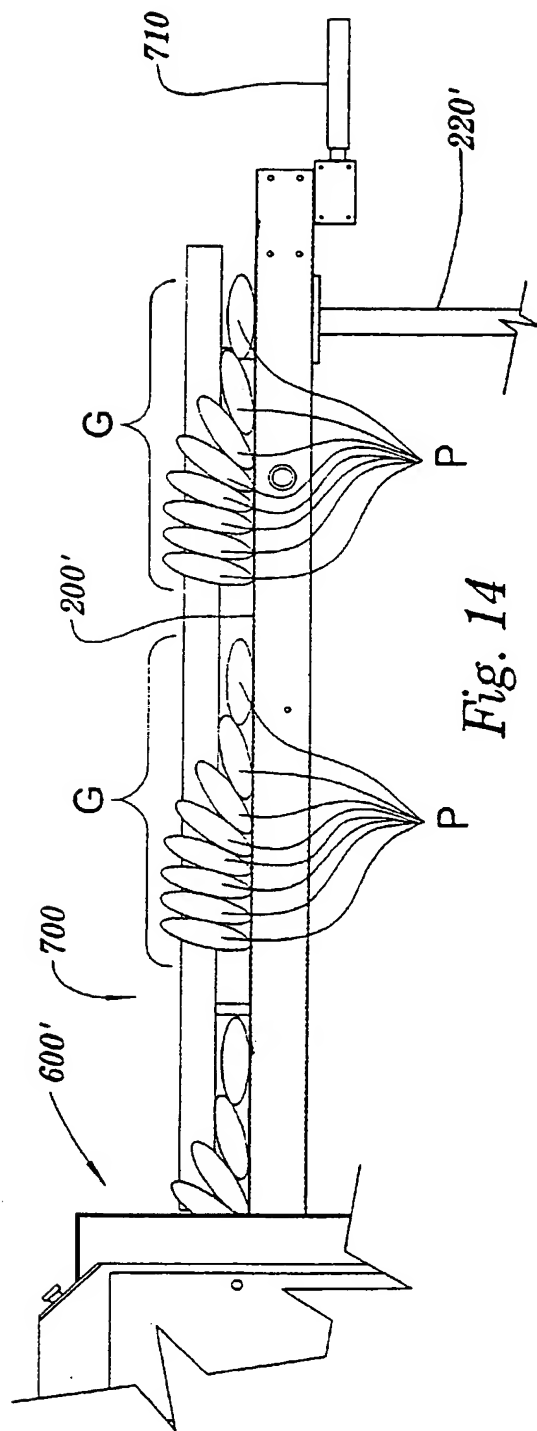
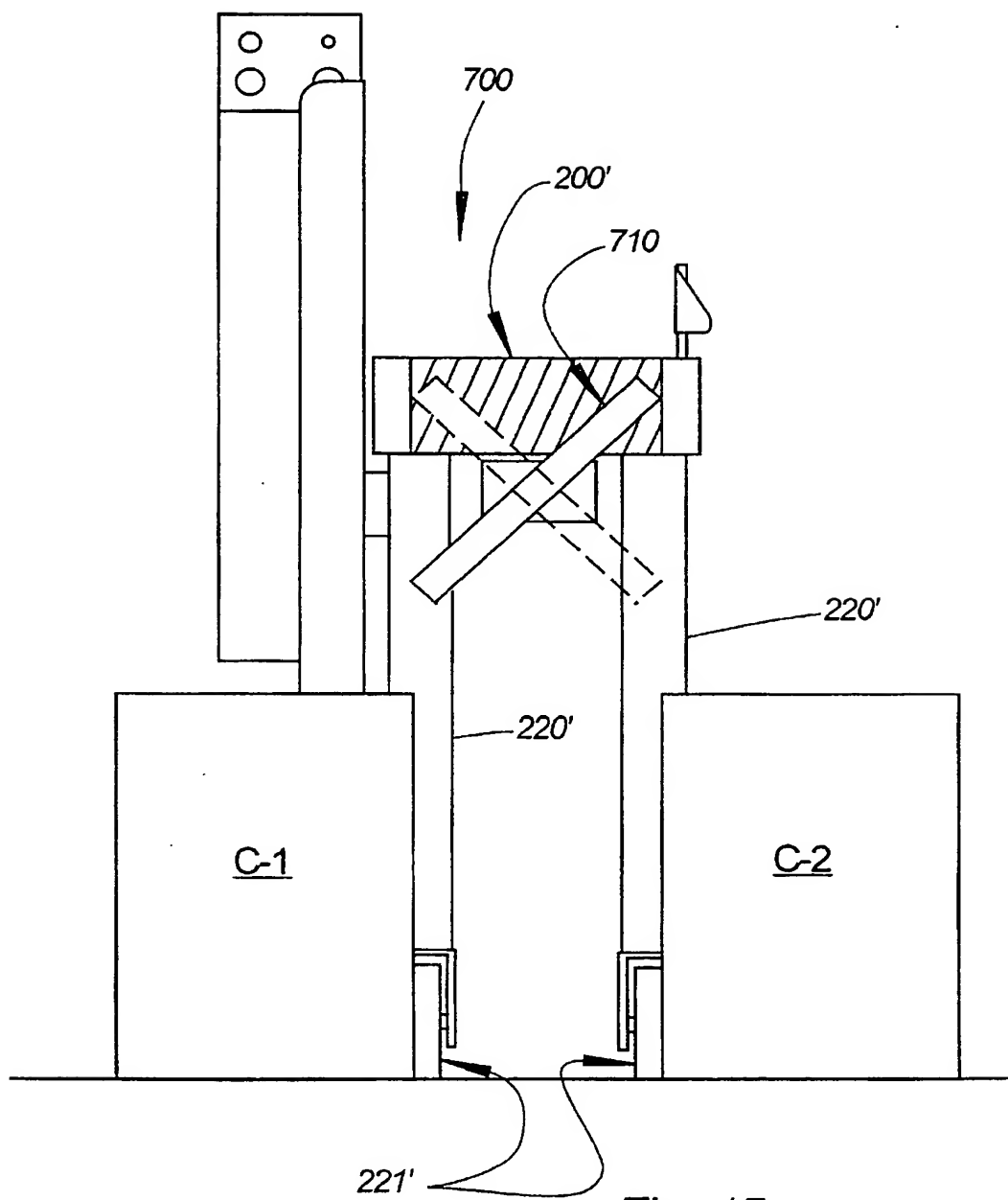


Fig. 14





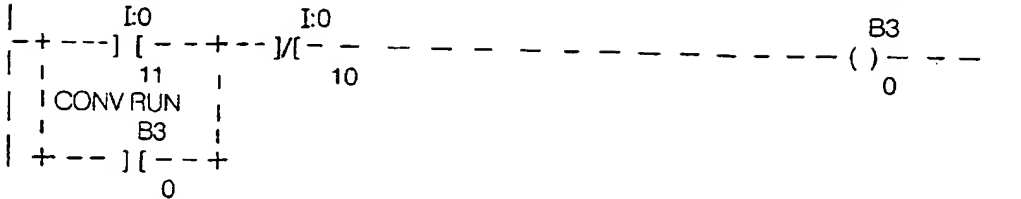
*Fig. 15*

RUNG 2:0 - START/STOP CONTROL.

RUNG 2:0 START PB E-STOP

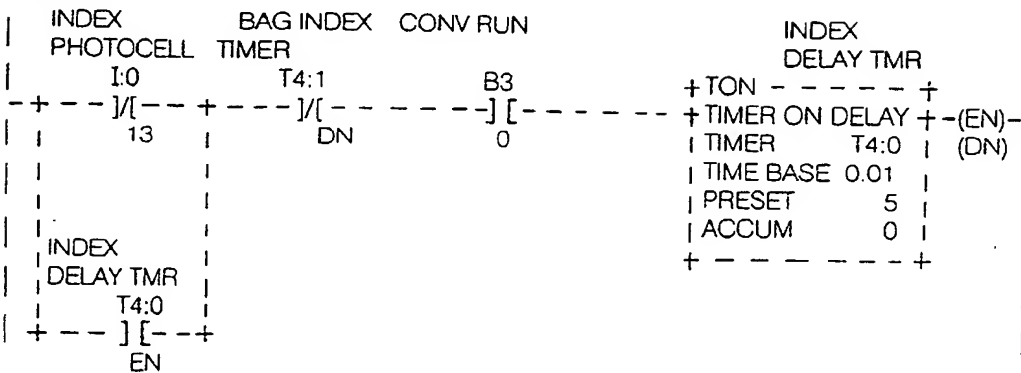
CONV

RUN

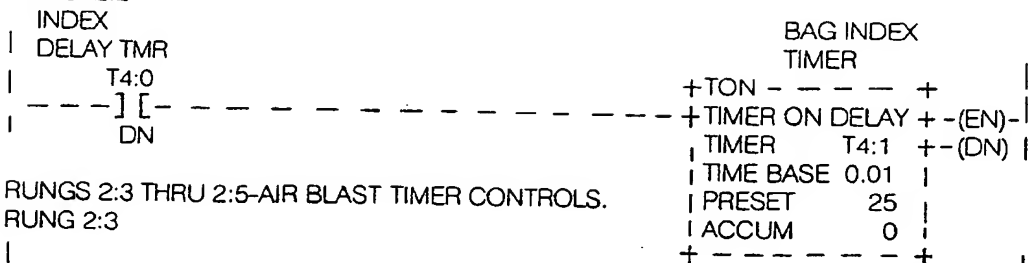


RUNG 2:1 AND 2:2 - INDEXING CONTROLS.

RUNG 2:1



RUNG 2:2



RUNGS 2:3 THRU 2:5-AIR BLAST TIMER CONTROLS.

RUNG 2:3

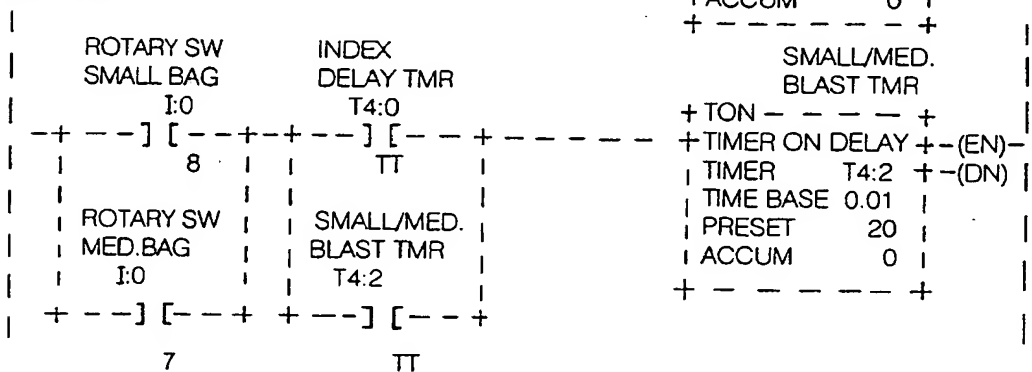


FIG.16A

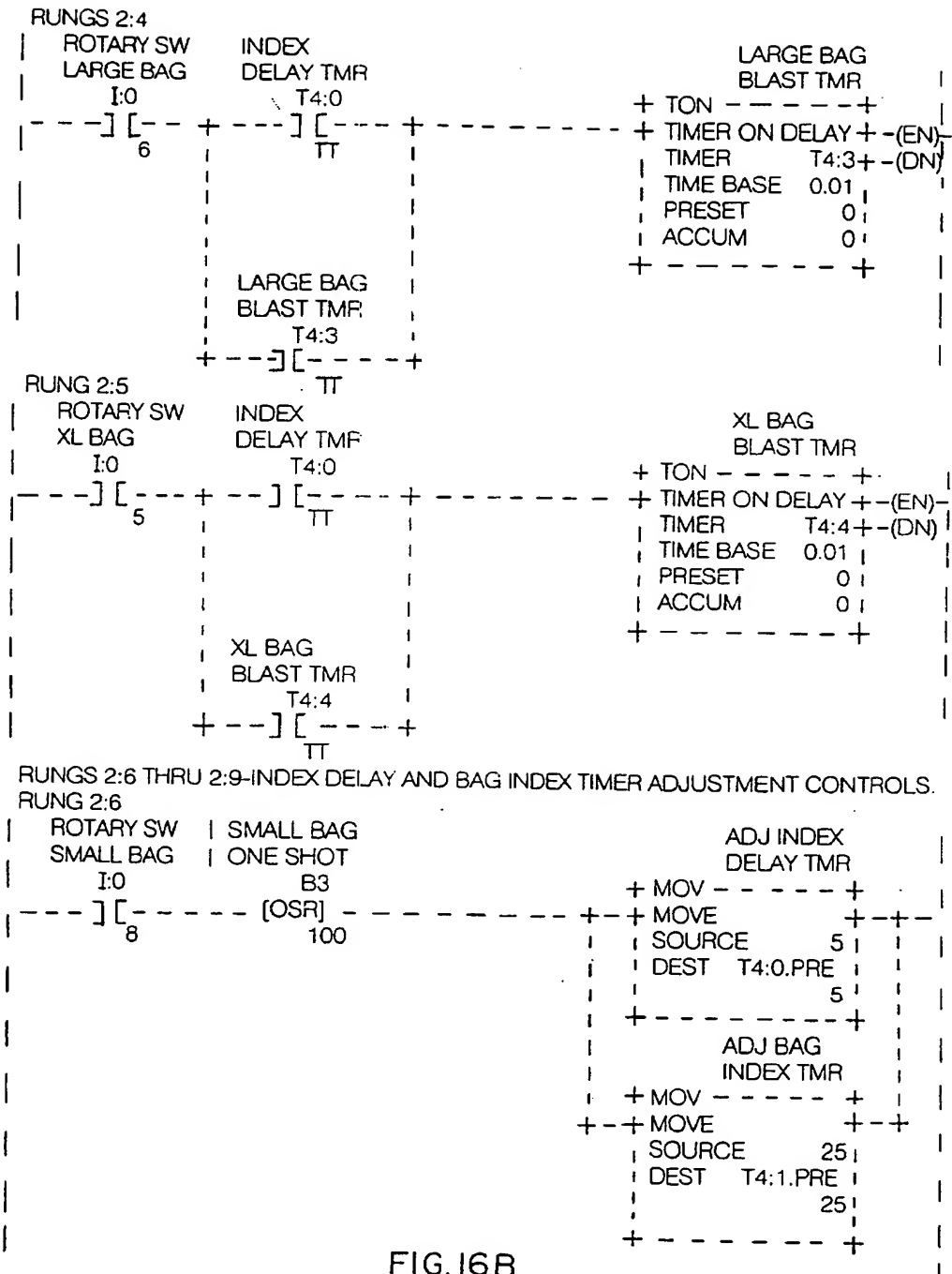
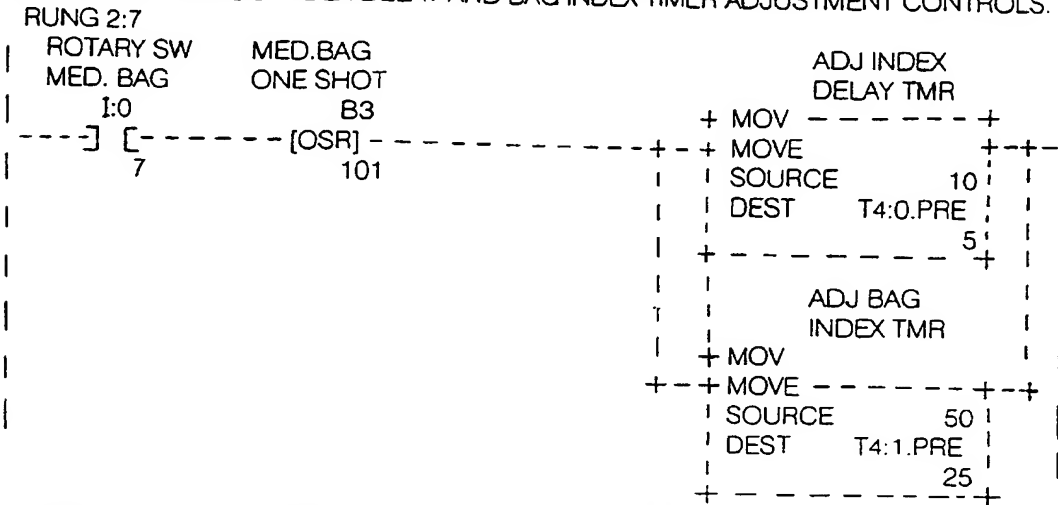


FIG.16B

RUNGS 2:6 THRU 2:9-INDEX DELAY AND BAG INDEX TIMER ADJUSTMENT CONTROLS.



RUNGS 2:6 THRU 2:9-INDEX DELAY AND BAG INDEX TIMER ADJUSTMENT CONTROLS.

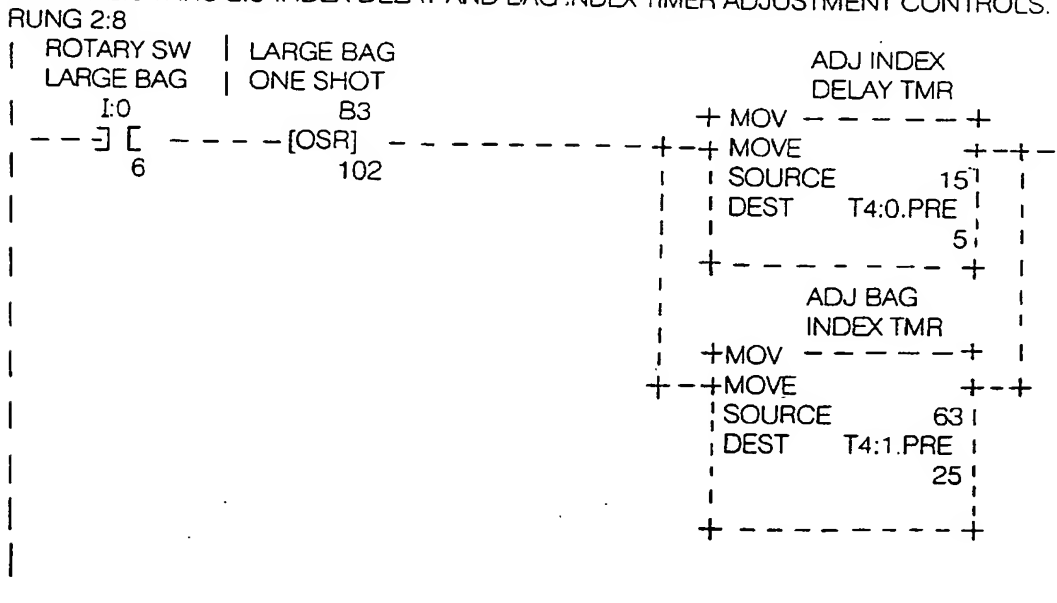
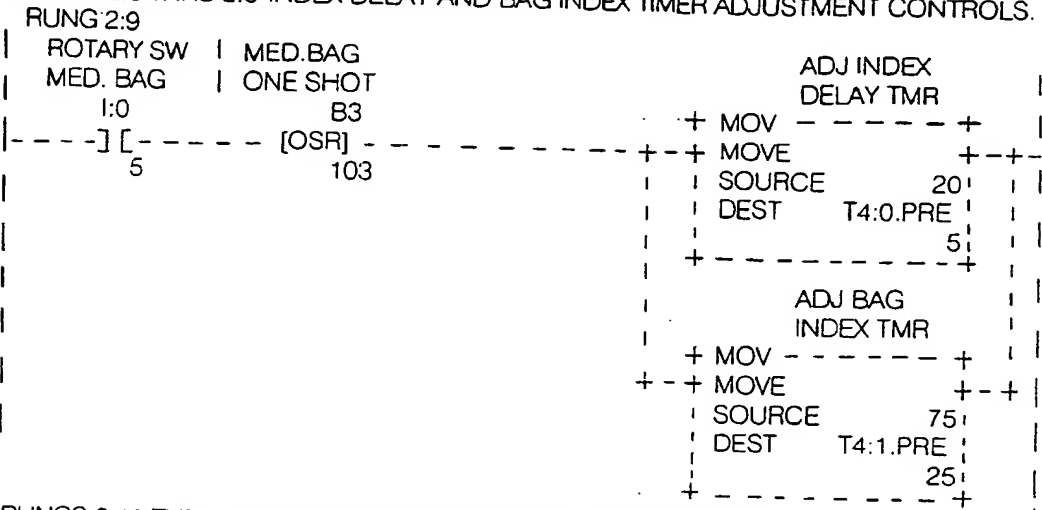


FIG. 16C

RUNGS 2:6 THRU 2:9-INDEX DELAY AND BAG INDEX TIMER ADJUSTMENT CONTROLS.



RUNGS 2:10 THRU 2:12-LONG/SHORT AIR BLAST TIME CONTROLS.

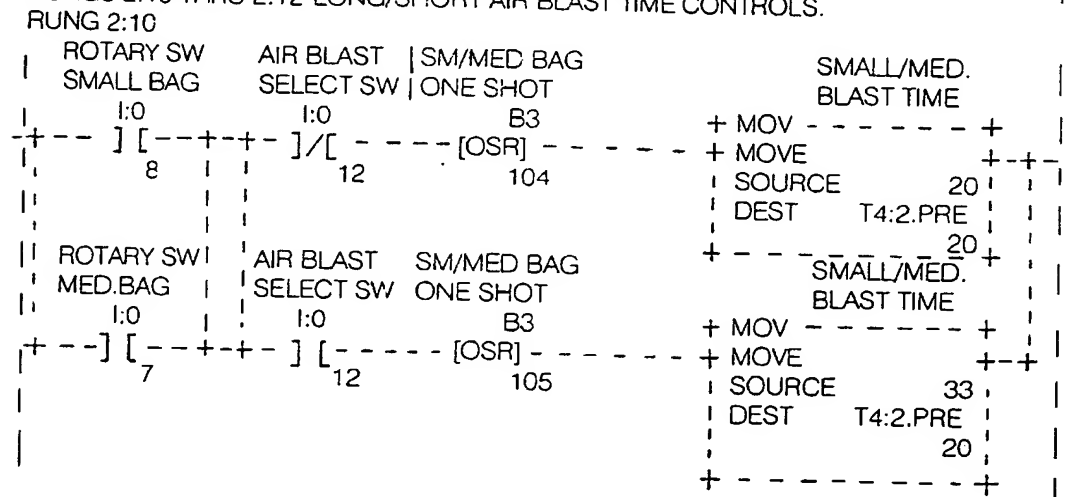


FIG. 16D

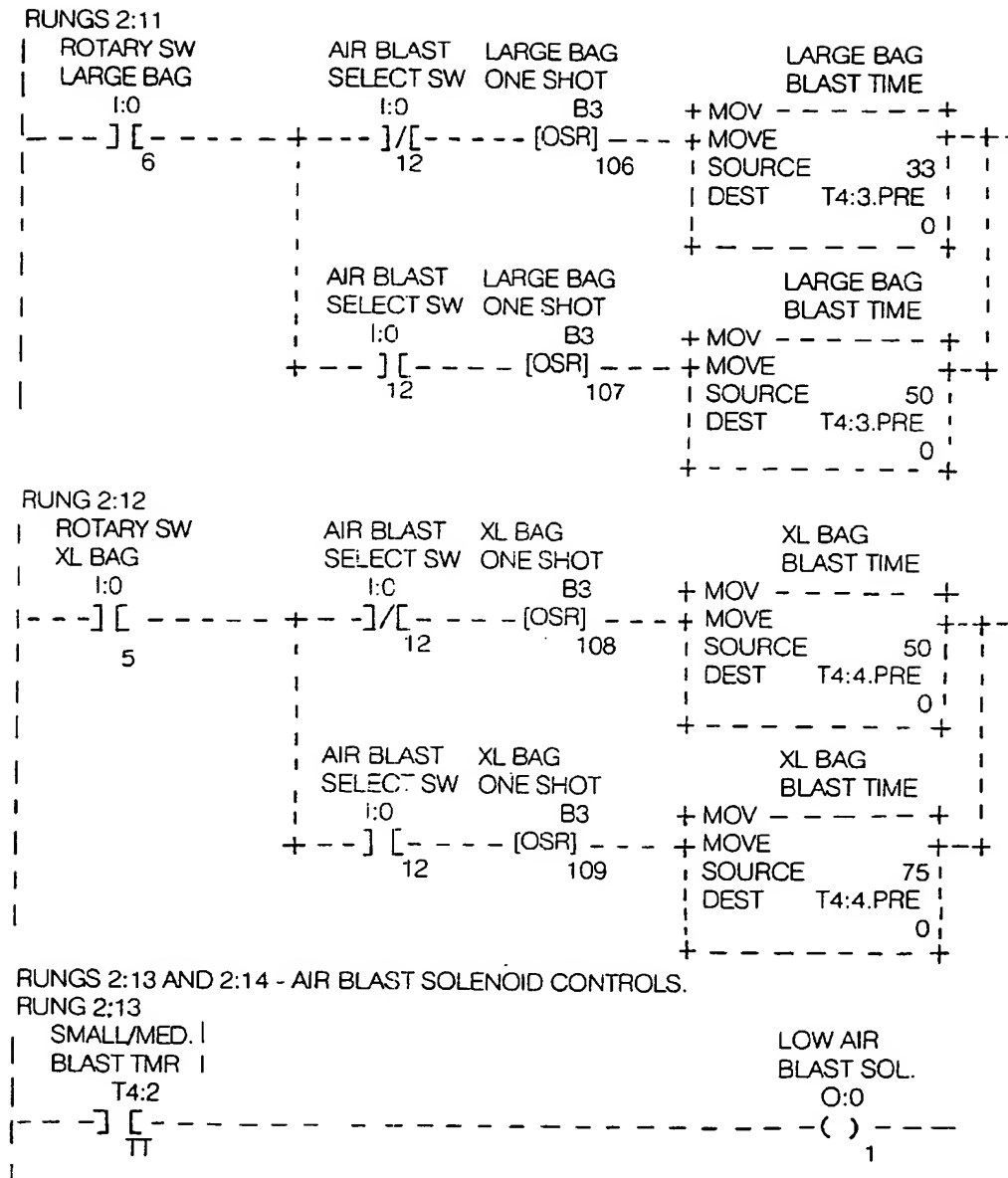


FIG. 16E

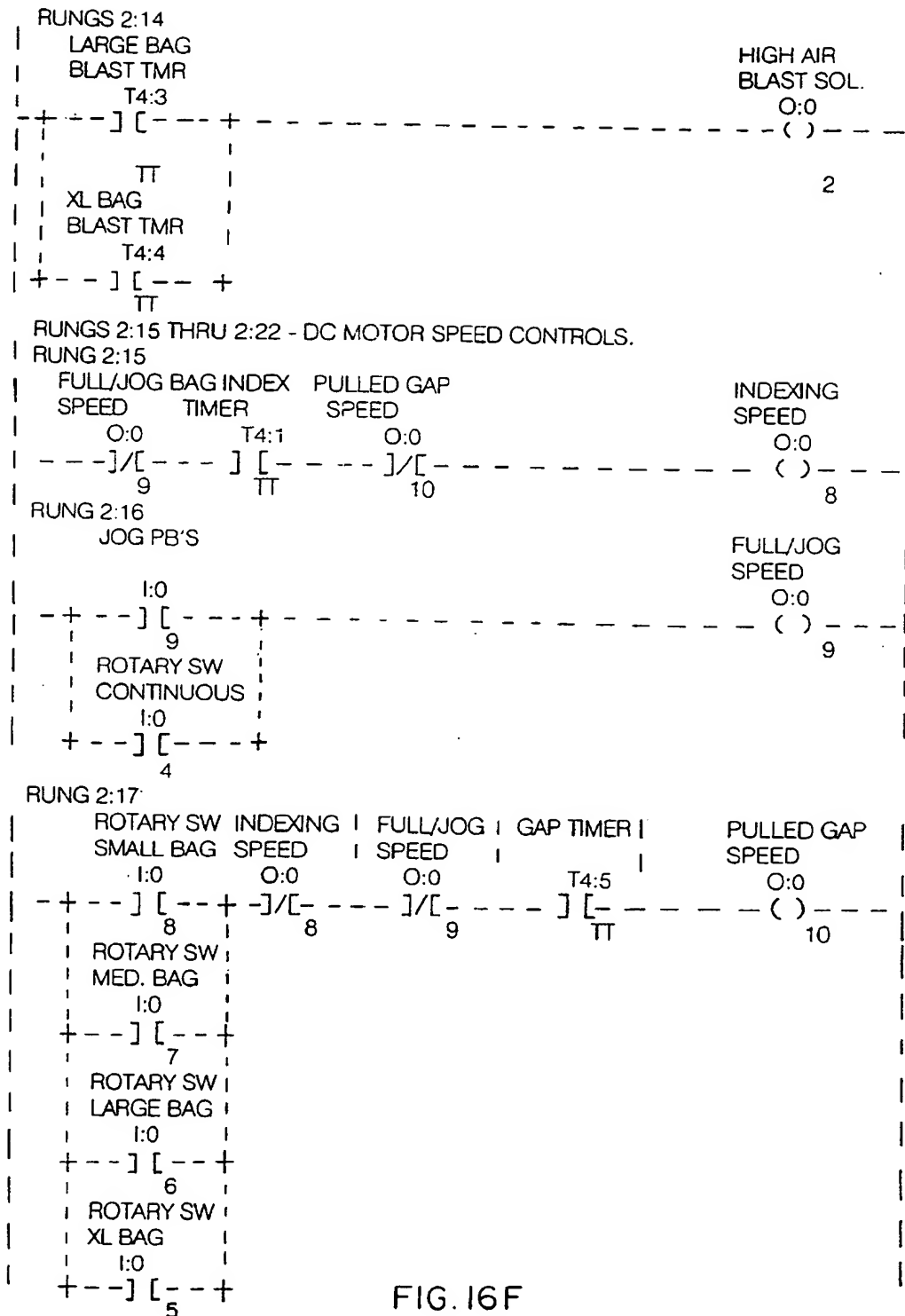


FIG. 16F

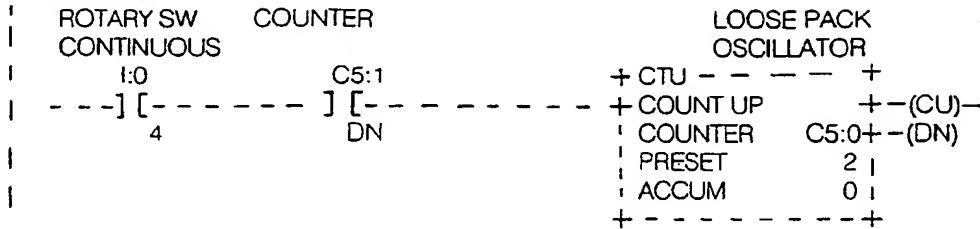


FIG. 16G

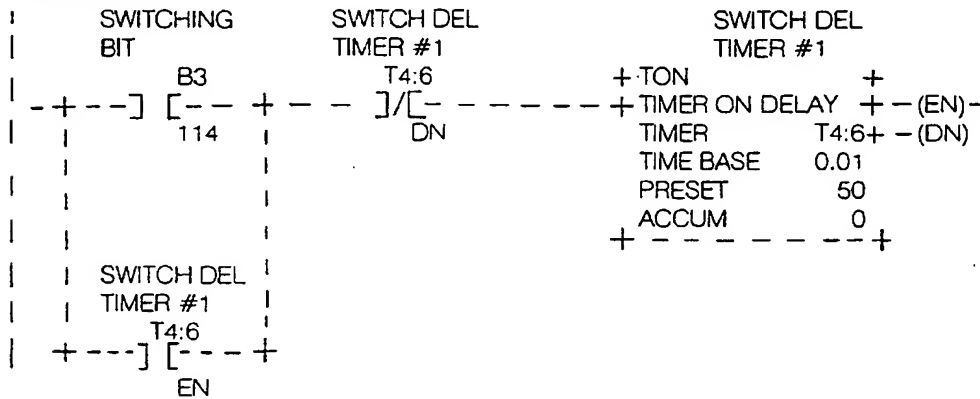


RUNGS 2:23 THRU 2:28 - OPTIONAL LOOSE PACK CONTROLS.

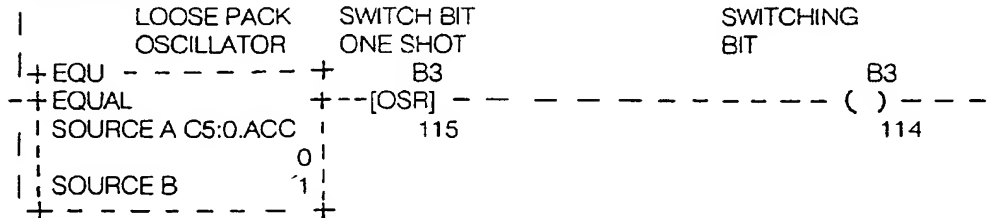
RUNG 2:23



RUNG 2:24



RUNG 2:25



RUNG 2:26

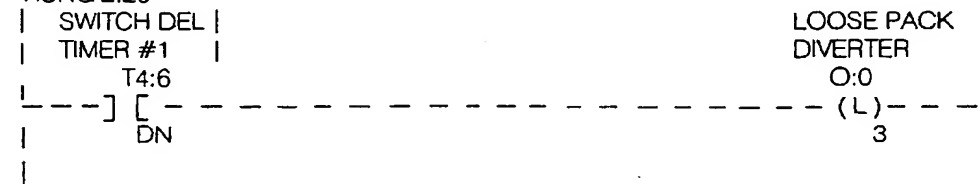
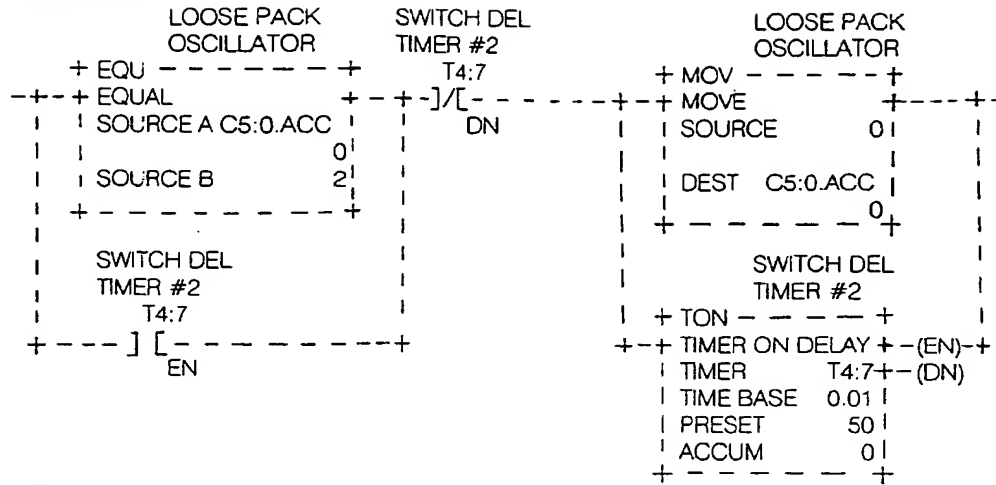
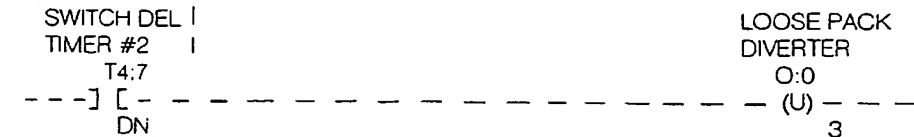


FIG. 16H

RUNGS 2:27



RUNG 2:28



RUNGS 2:29 THRU 2:32-STROBE CONTROLS. THE OUTPUTS WILL STROBE ONCE WHENEVER THE START BUTTON IS PRESSED, OR WHEN THE COUNTER REACHES IT'S COUNT. THIS WILL LOAD THE VALUE ON THE THUMBWHEEL SWITCHES INTO MEMORY.

RUNG 2:29

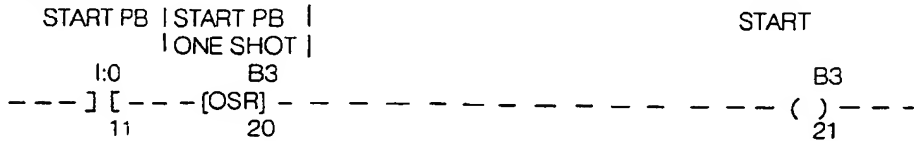


FIG.16I

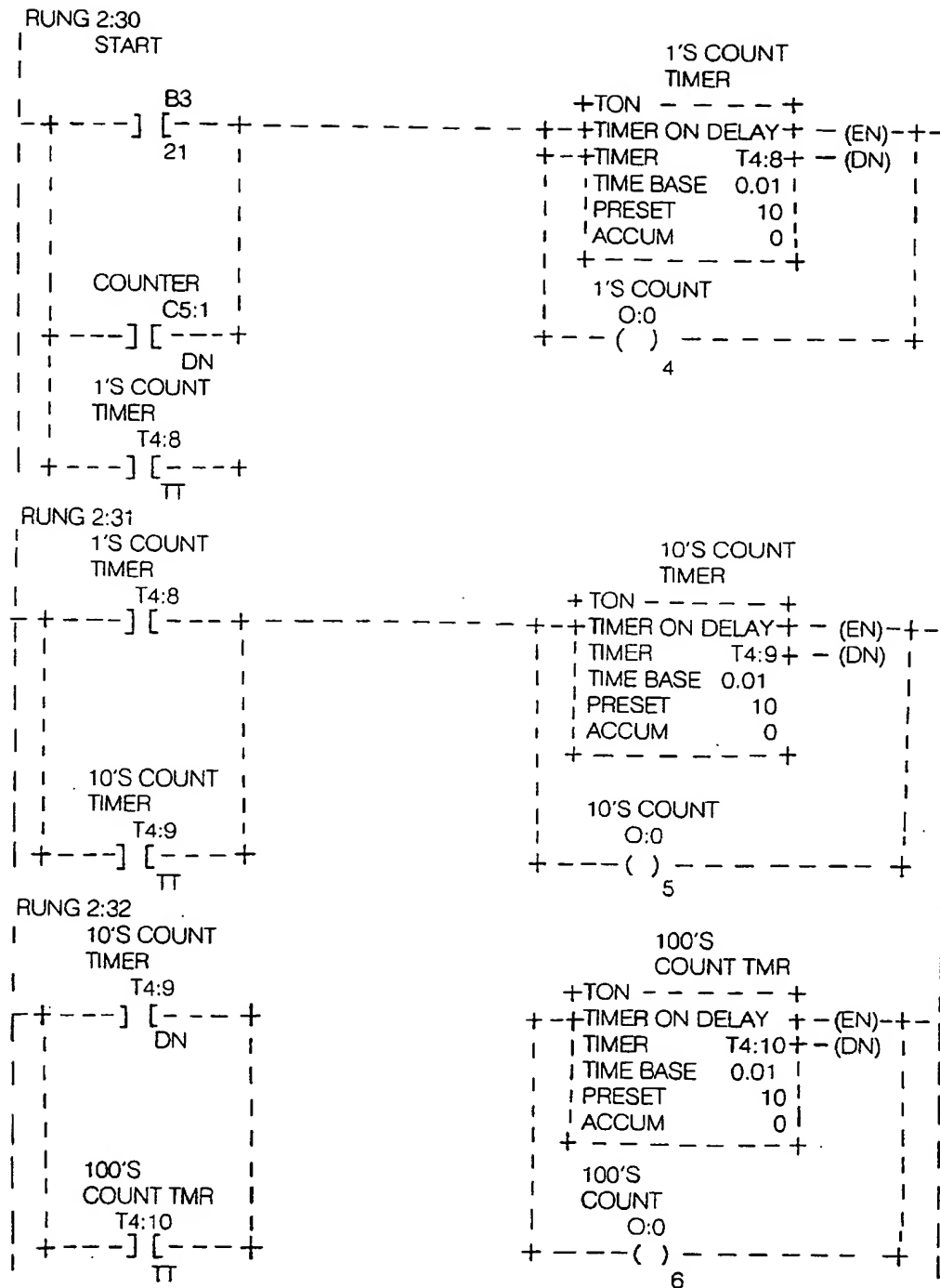
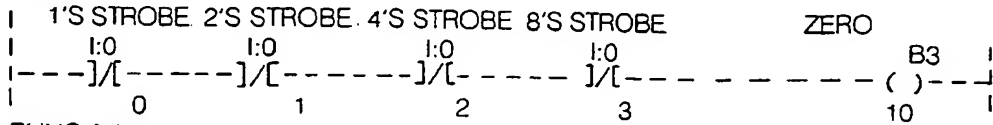


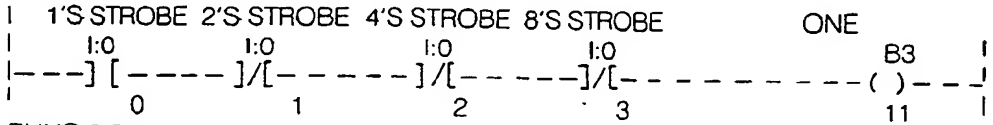
FIG. 16J

RUNGS 2:33 THRU 2:42-NUMBER CONTROL FOR ALL THUMBWHEEL SWITCHES.

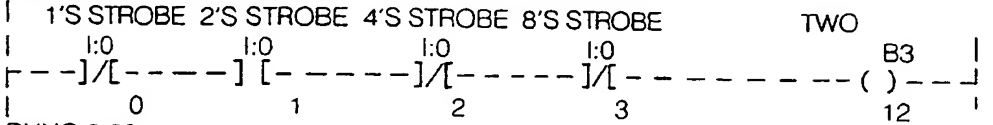
RUNG 2:33



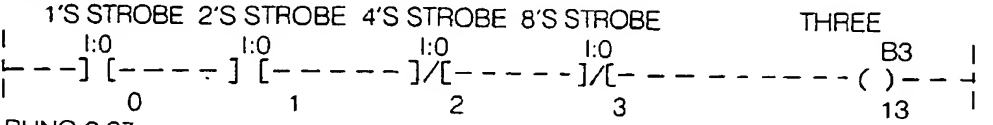
RUNG 2:34



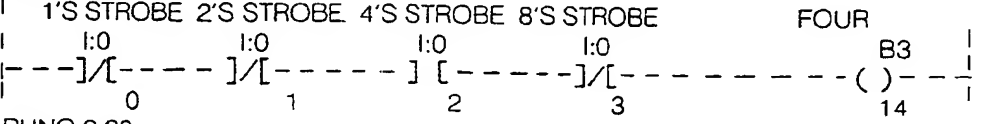
RUNG 2:35



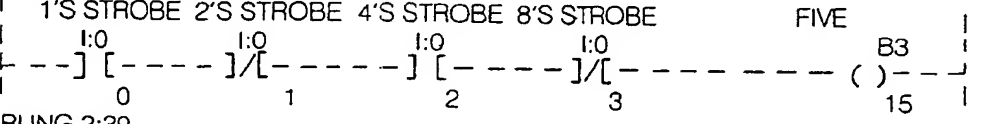
RUNG 2:36



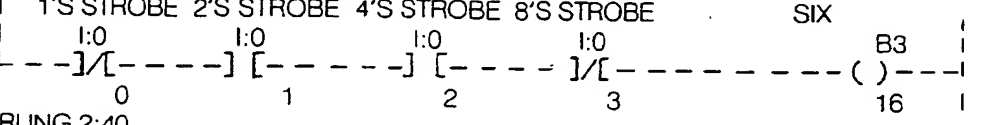
RUNG 2:37



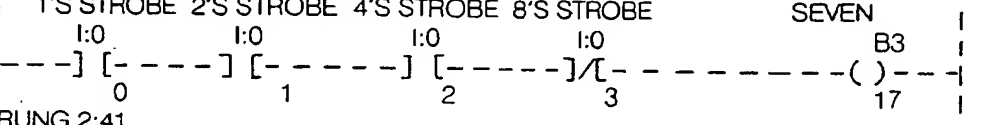
RUNG 2:38



RUNG 2:39



RUNG 2:40



RUNG 2:41

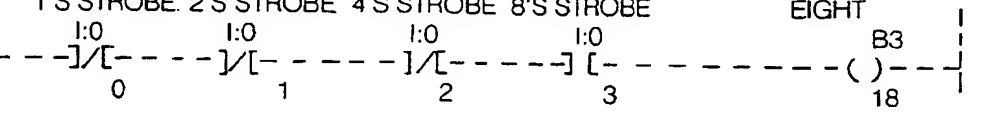


FIG. 16K

1'S STROBE 2'S STROBE 4'S STROBE 8'S STROBE NINE B3

0 1 2 3 19

RUNG 2:43

FIG. 16L

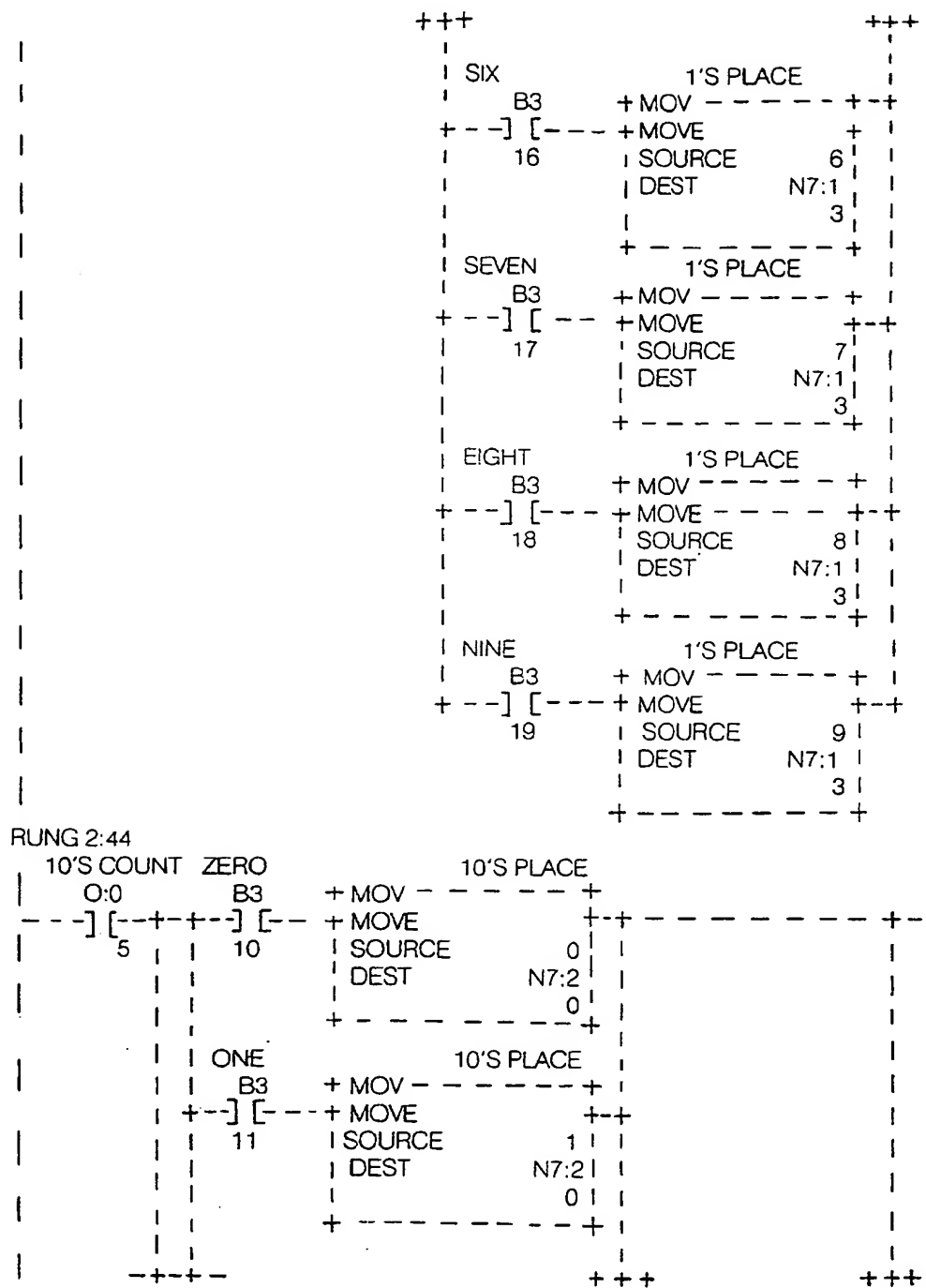


FIG. 16M

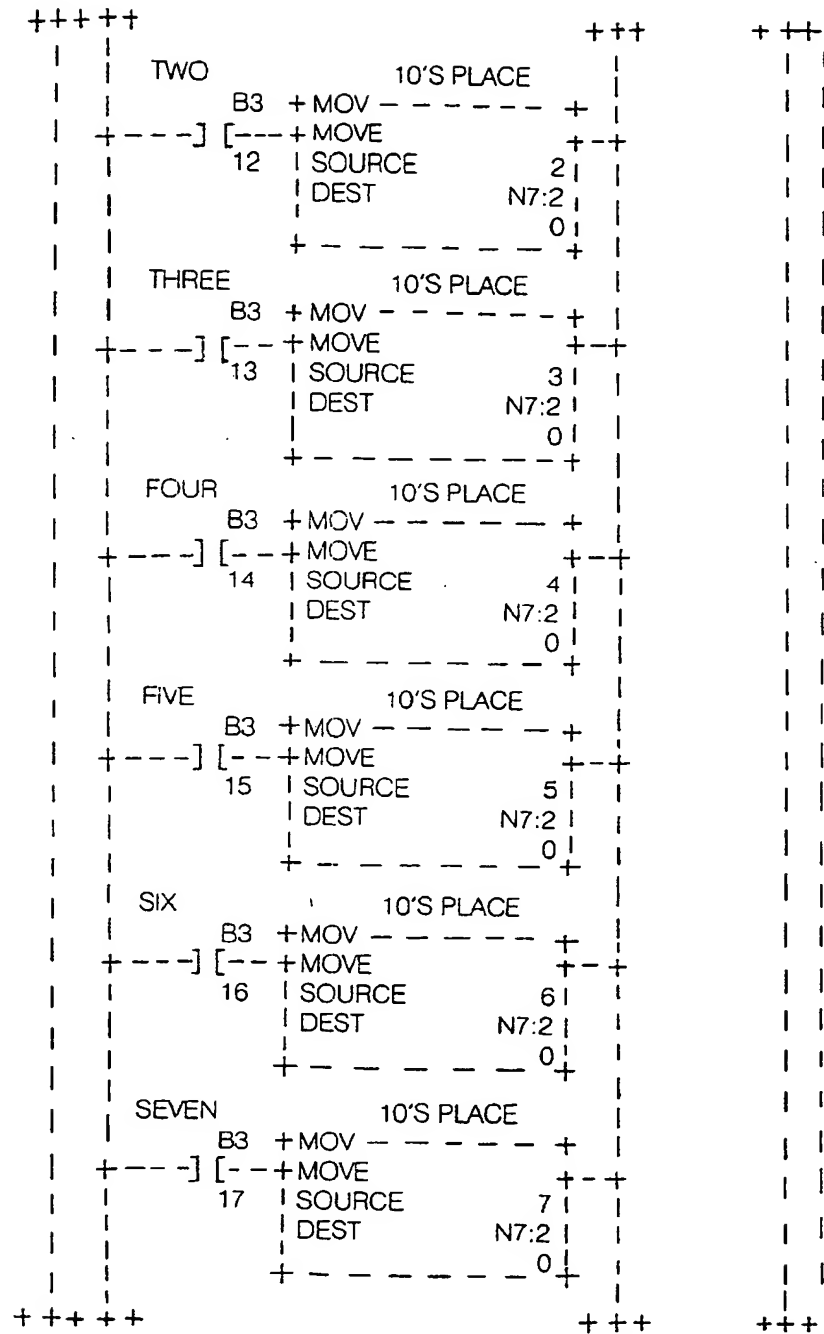


FIG. 16N

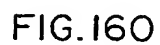


FIG.160



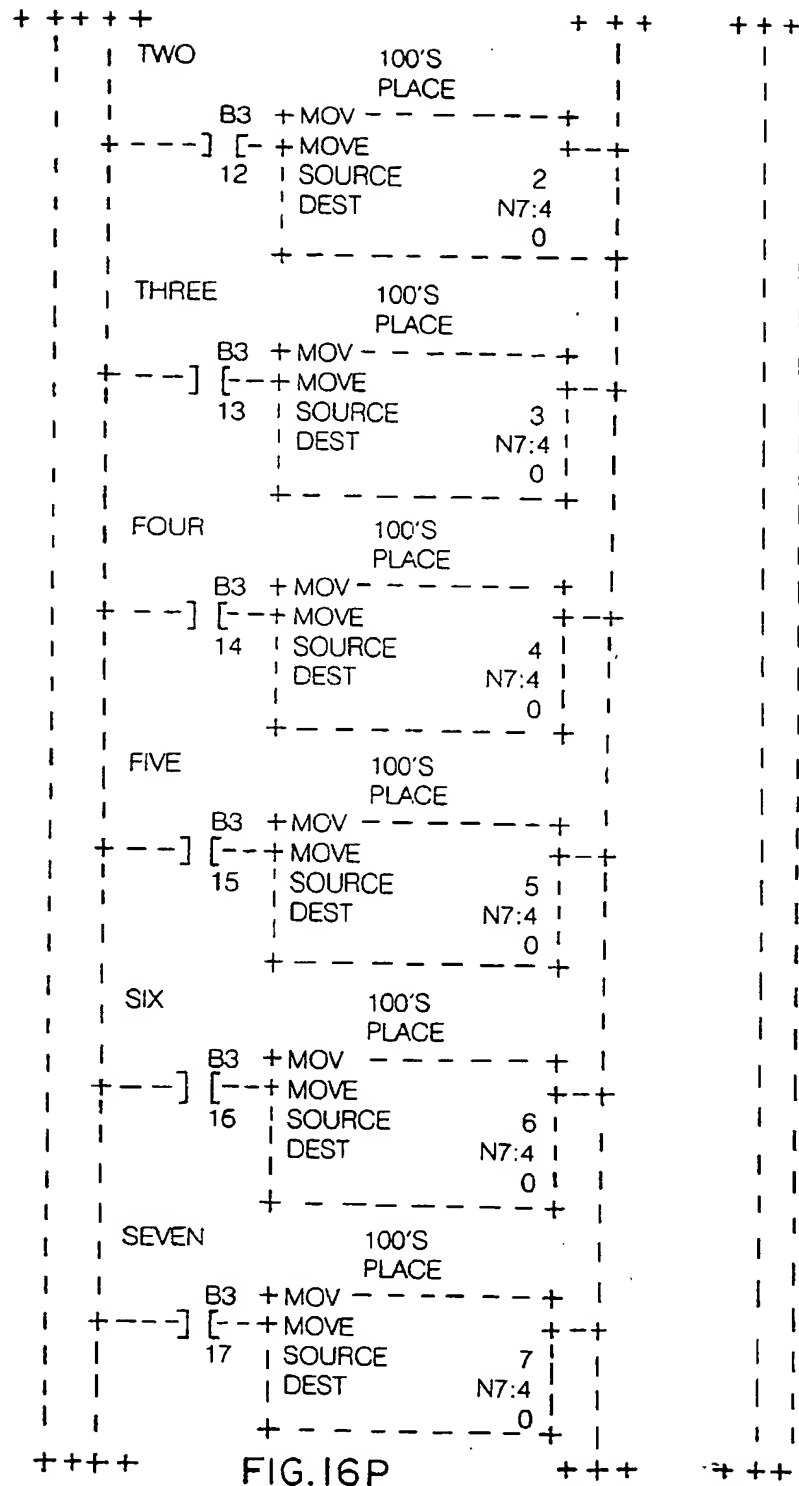


FIG.16P

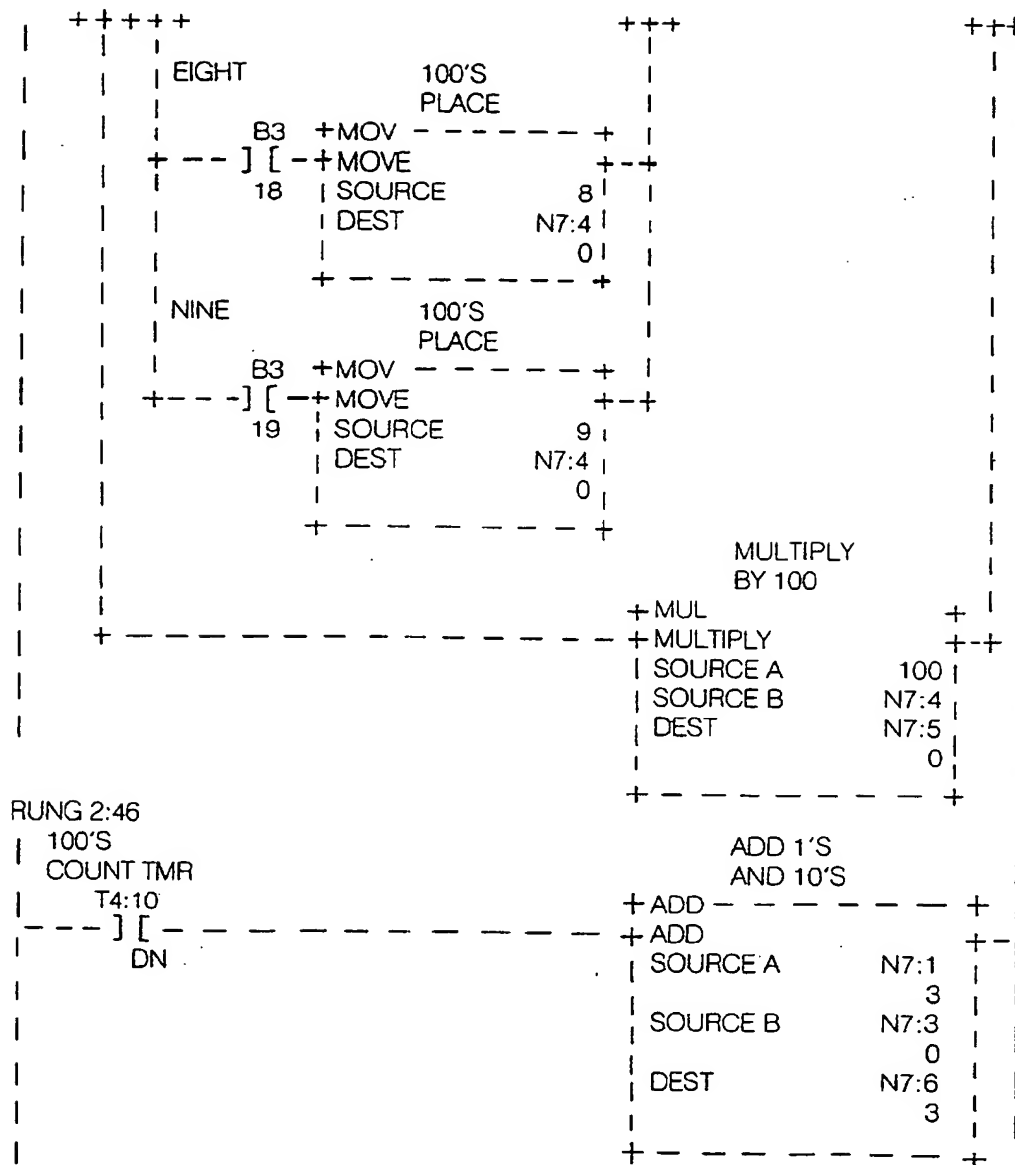


FIG. 16Q



RUNG 2:51

RUNG 2:52

FIG. 16S

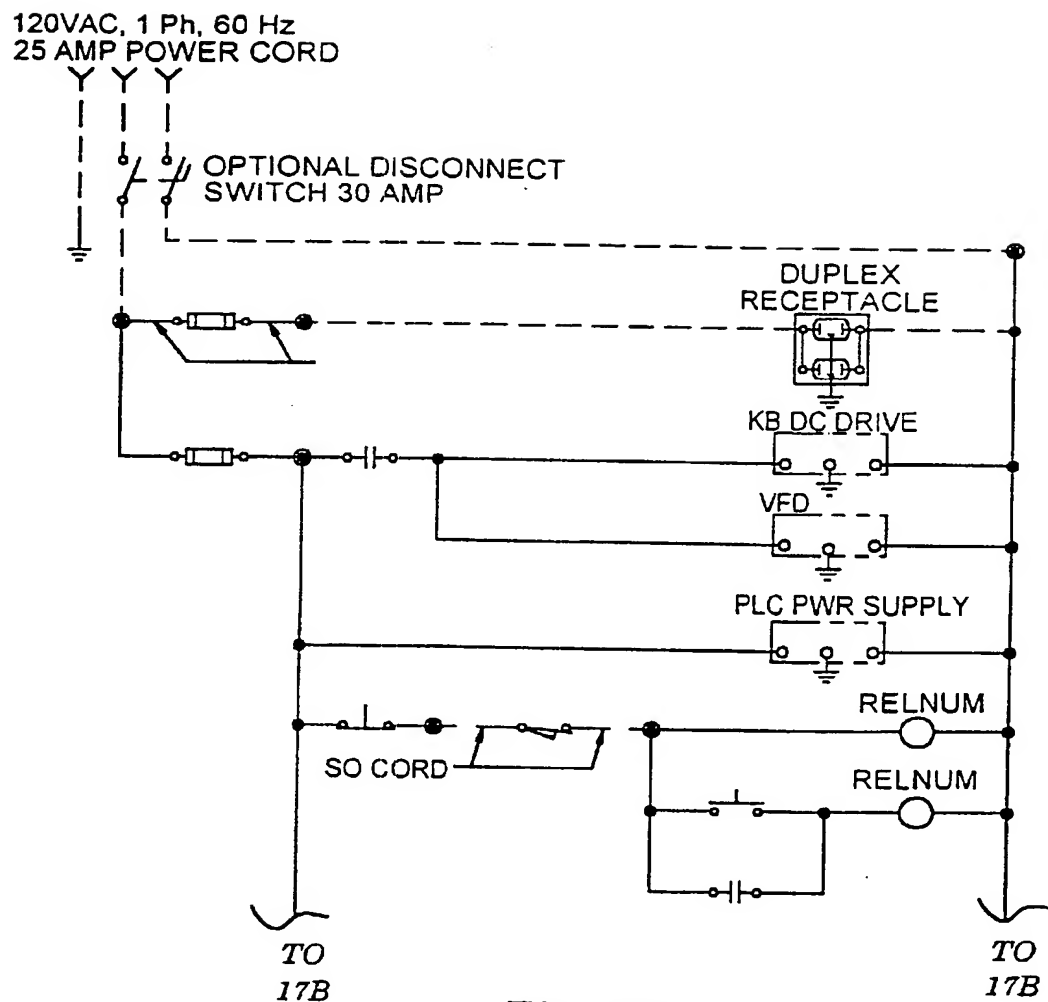
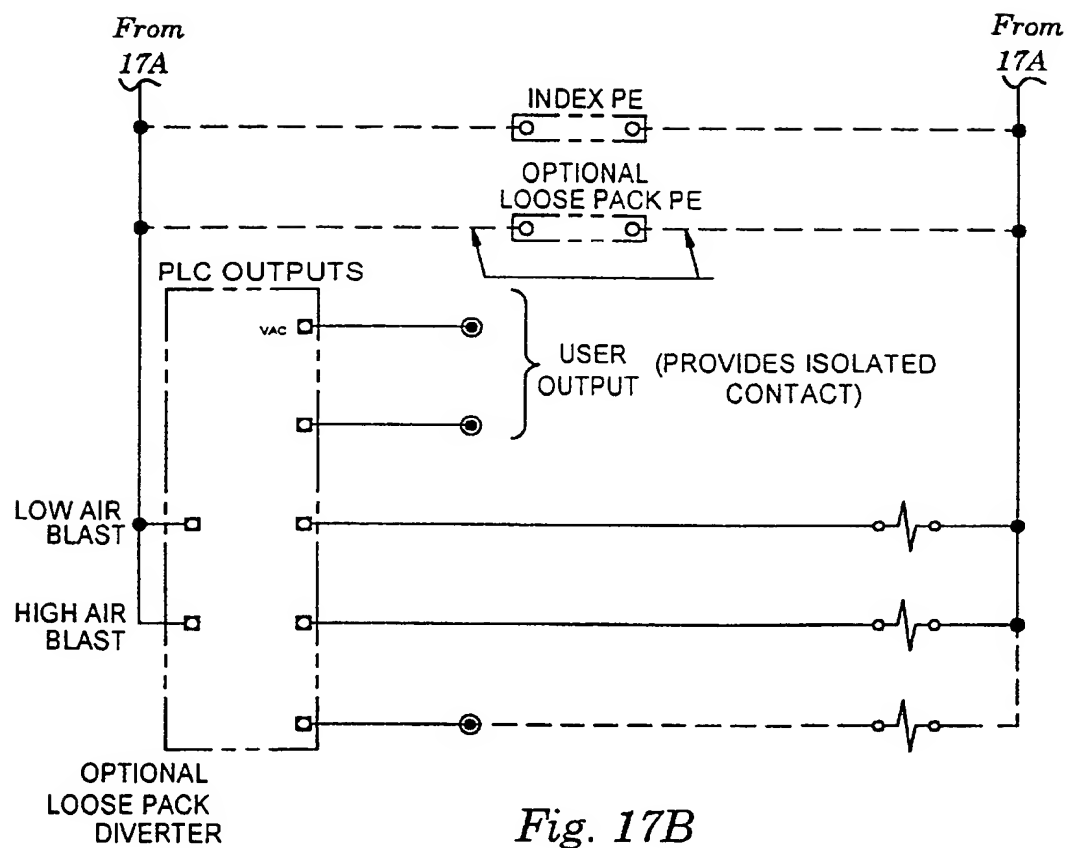


Fig. 17A



*Fig. 17B*

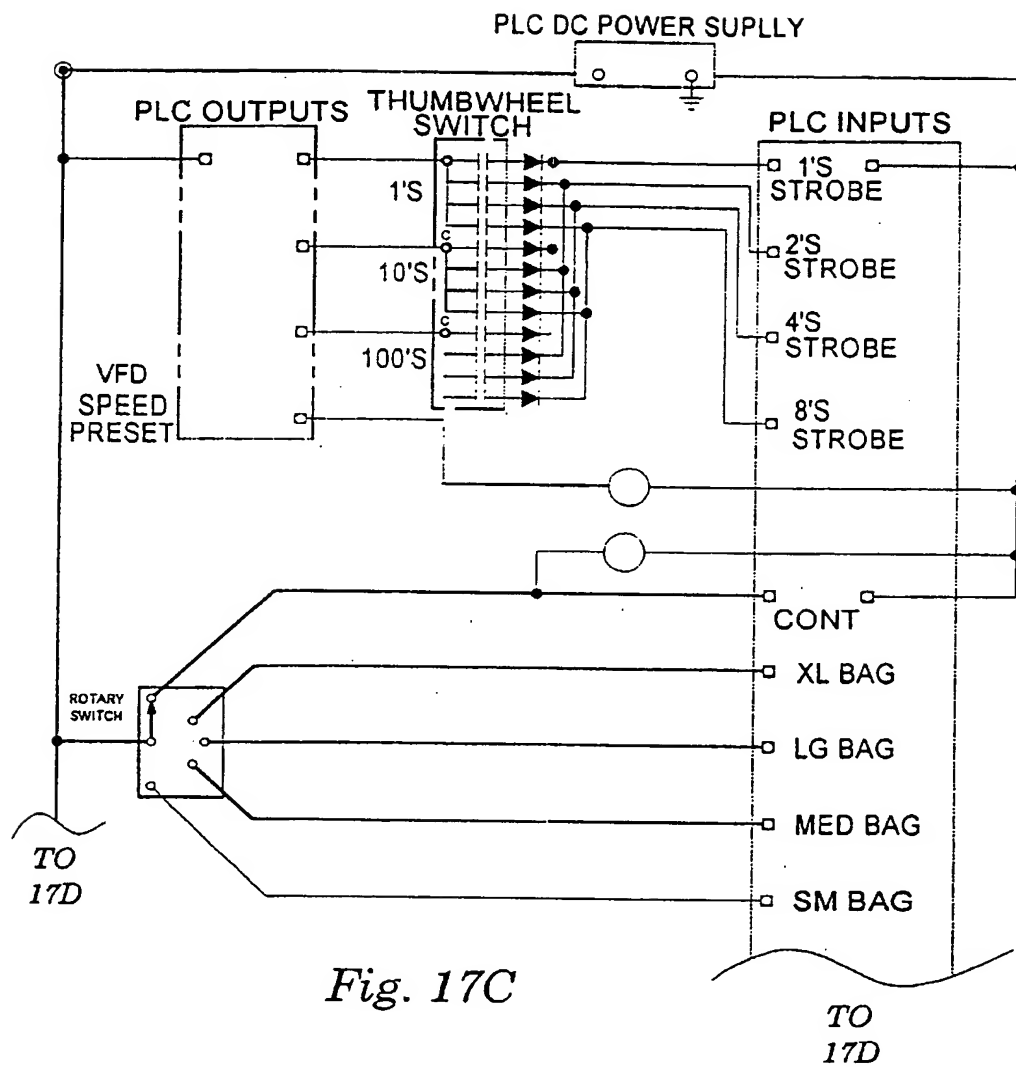
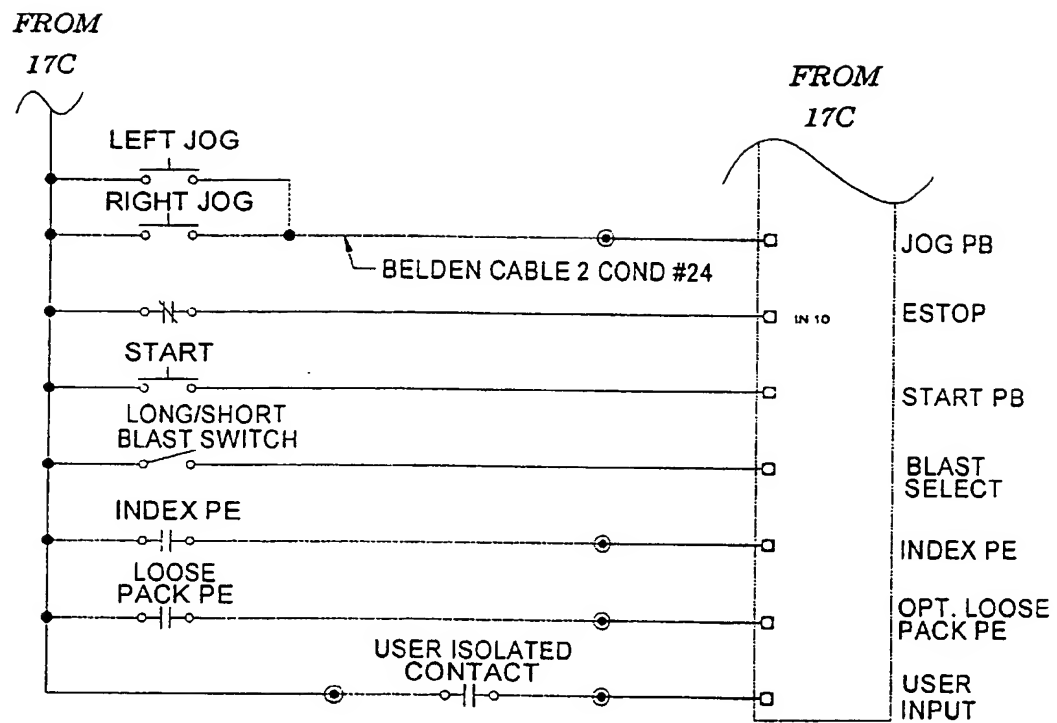


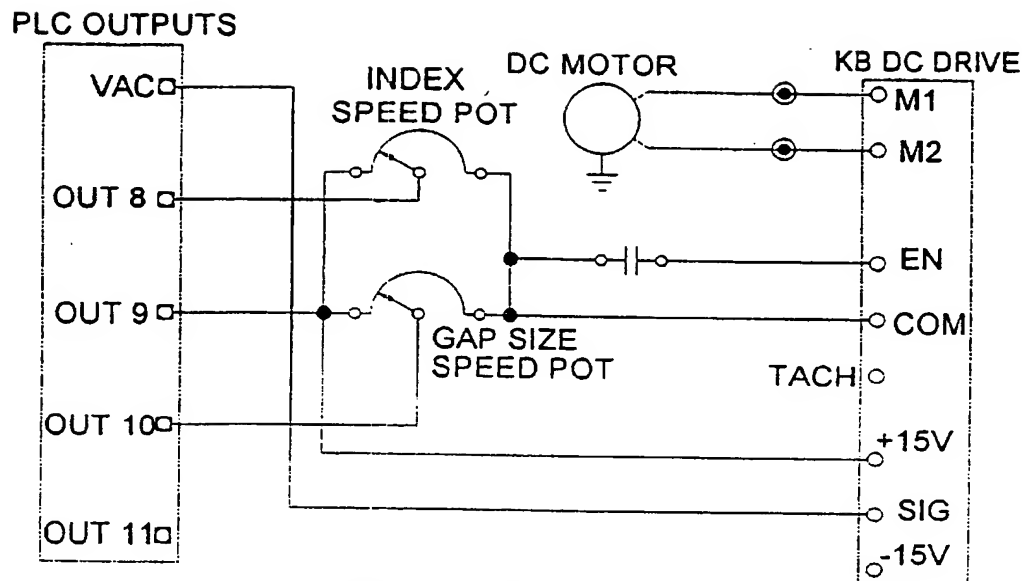
Fig. 17C

TO  
17D

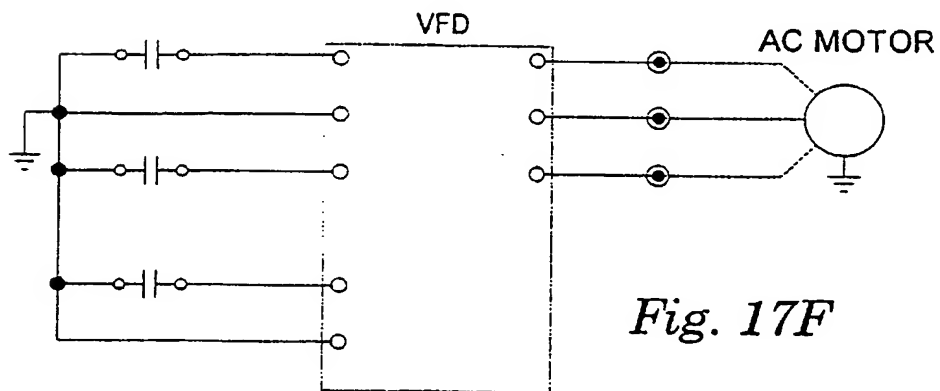


*Fig. 17D*

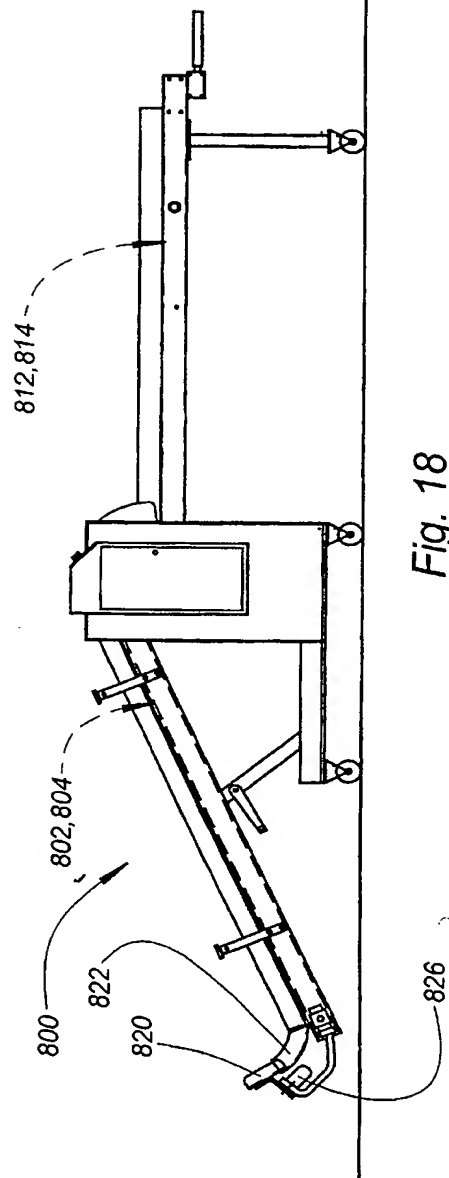
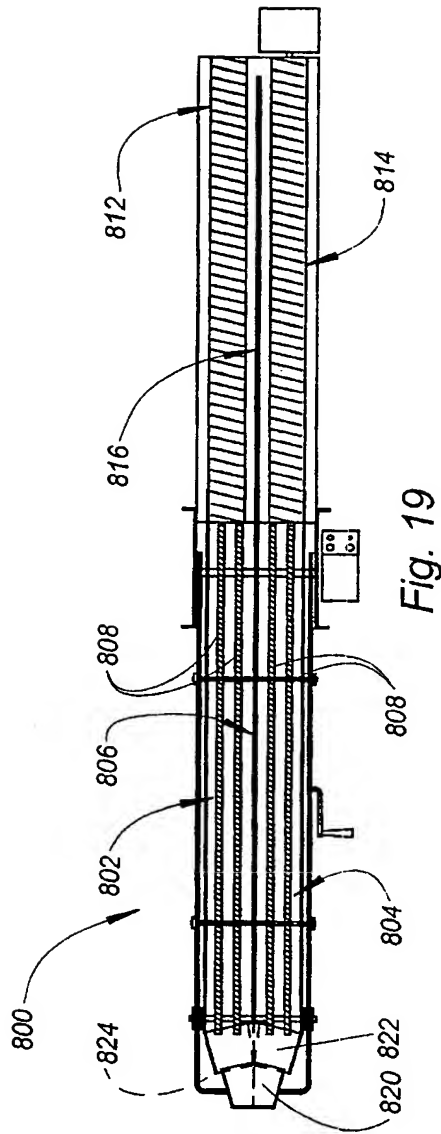


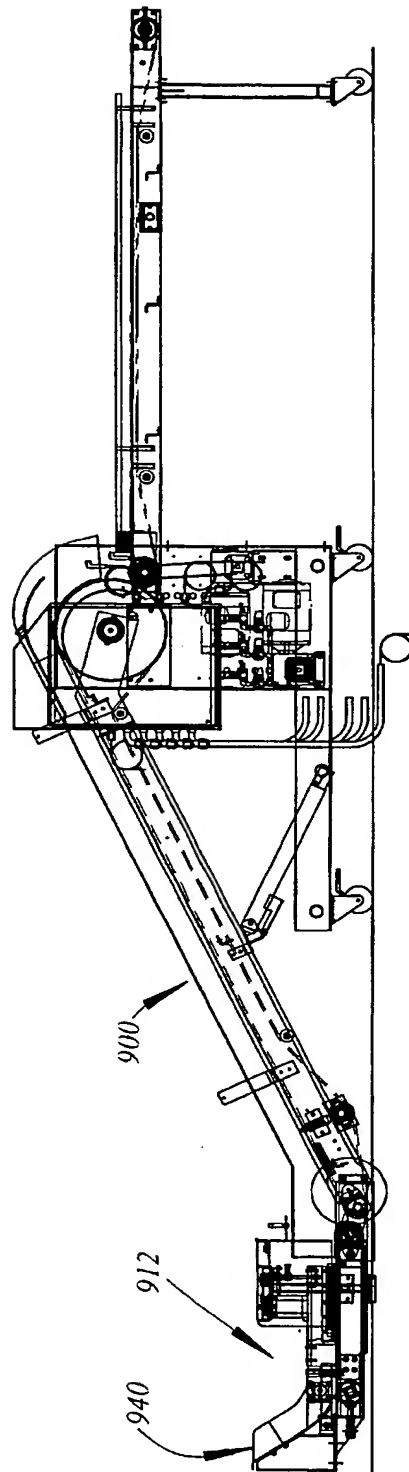
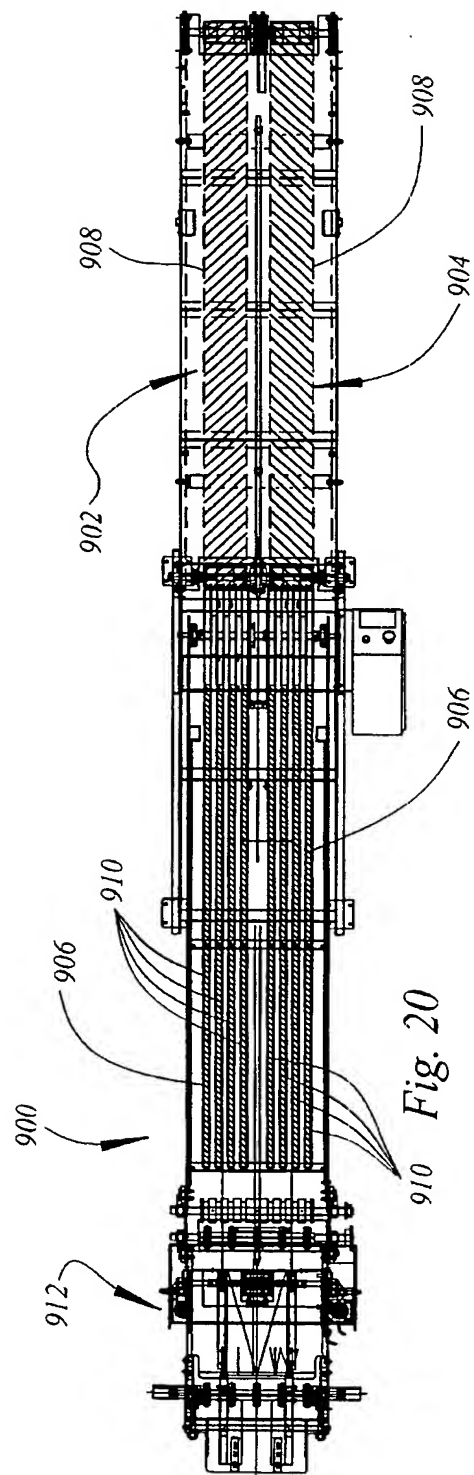


*Fig. 17E*



*Fig. 17F*





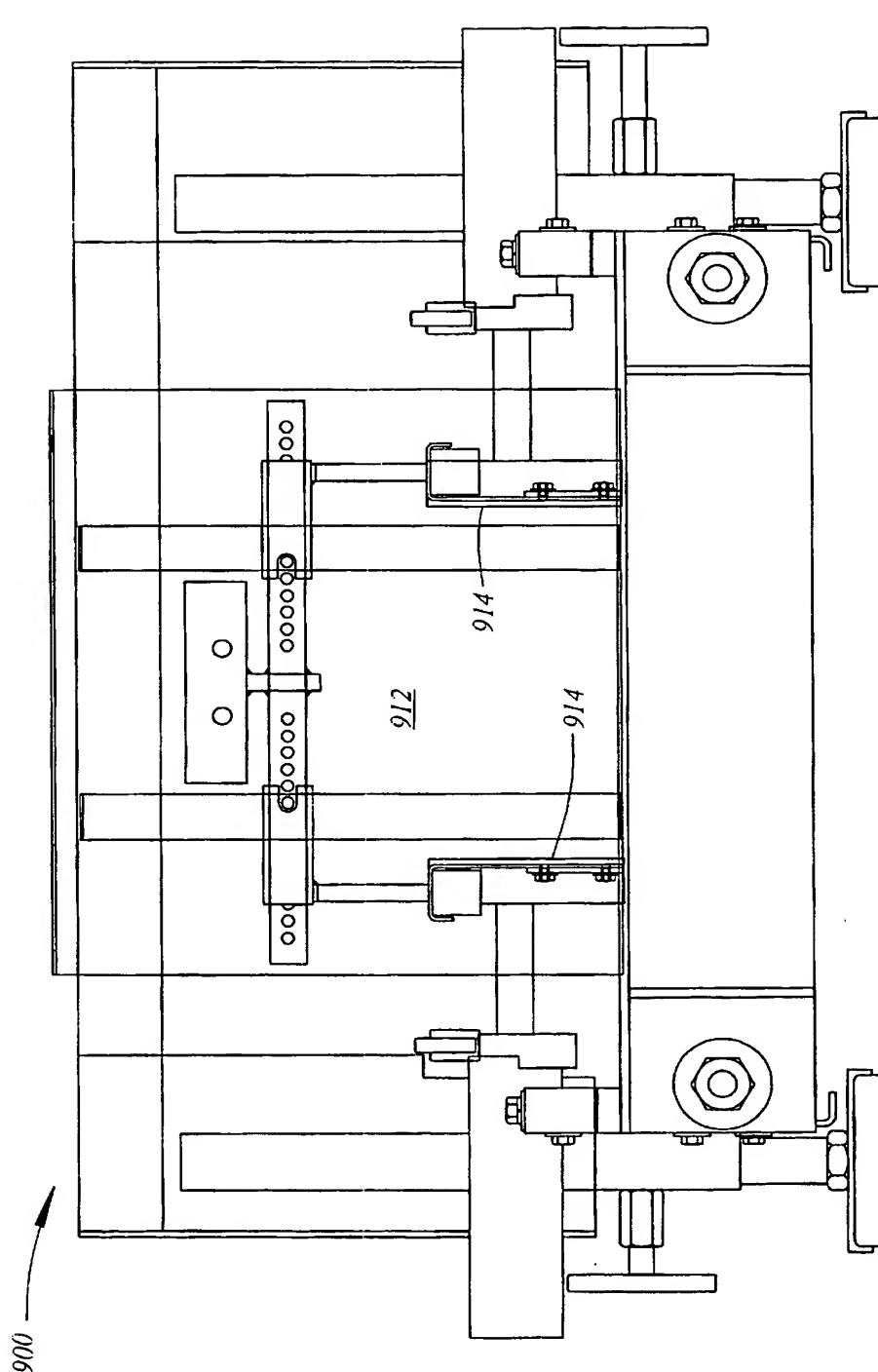


Fig. 22

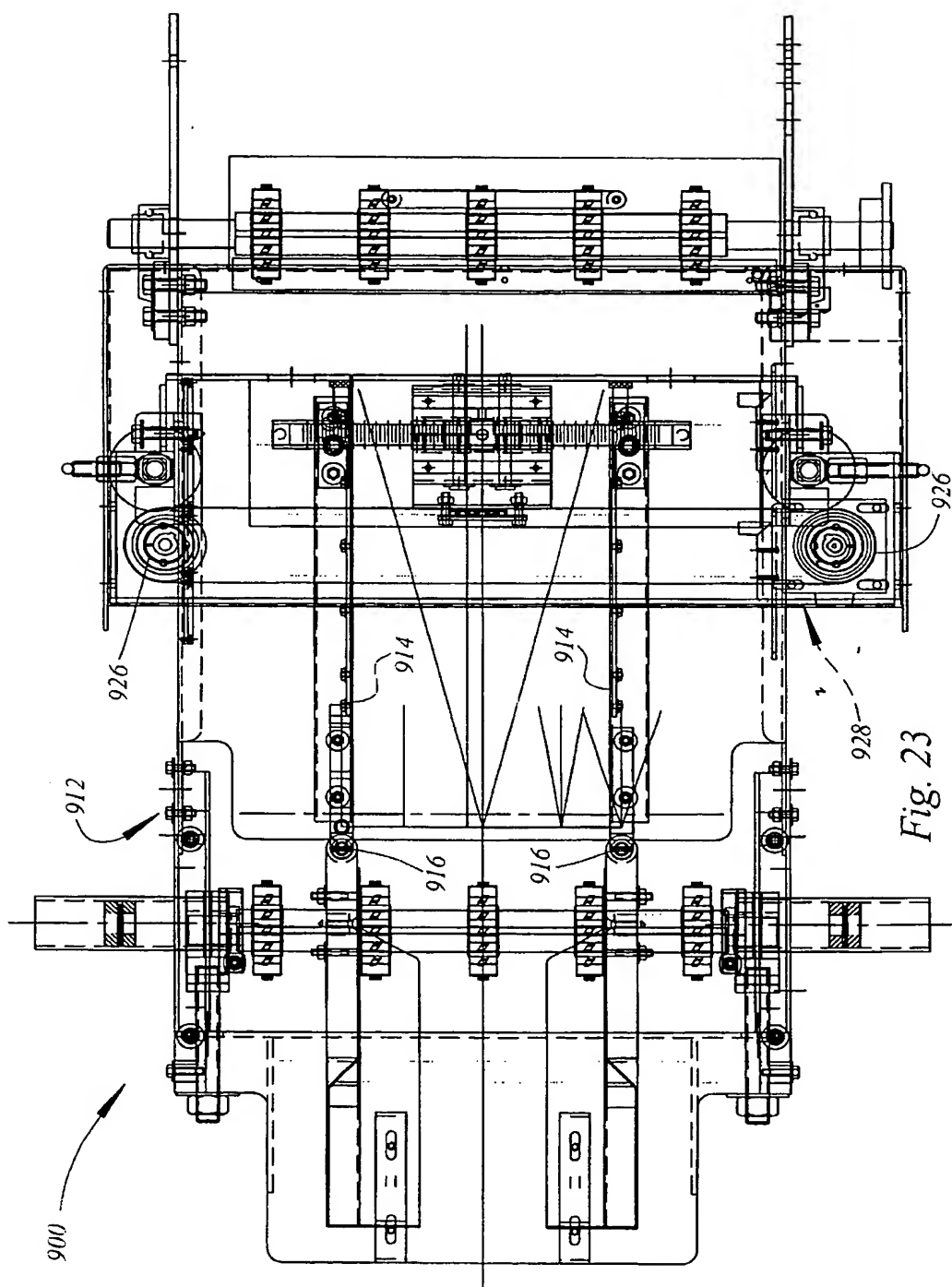


Fig. 23

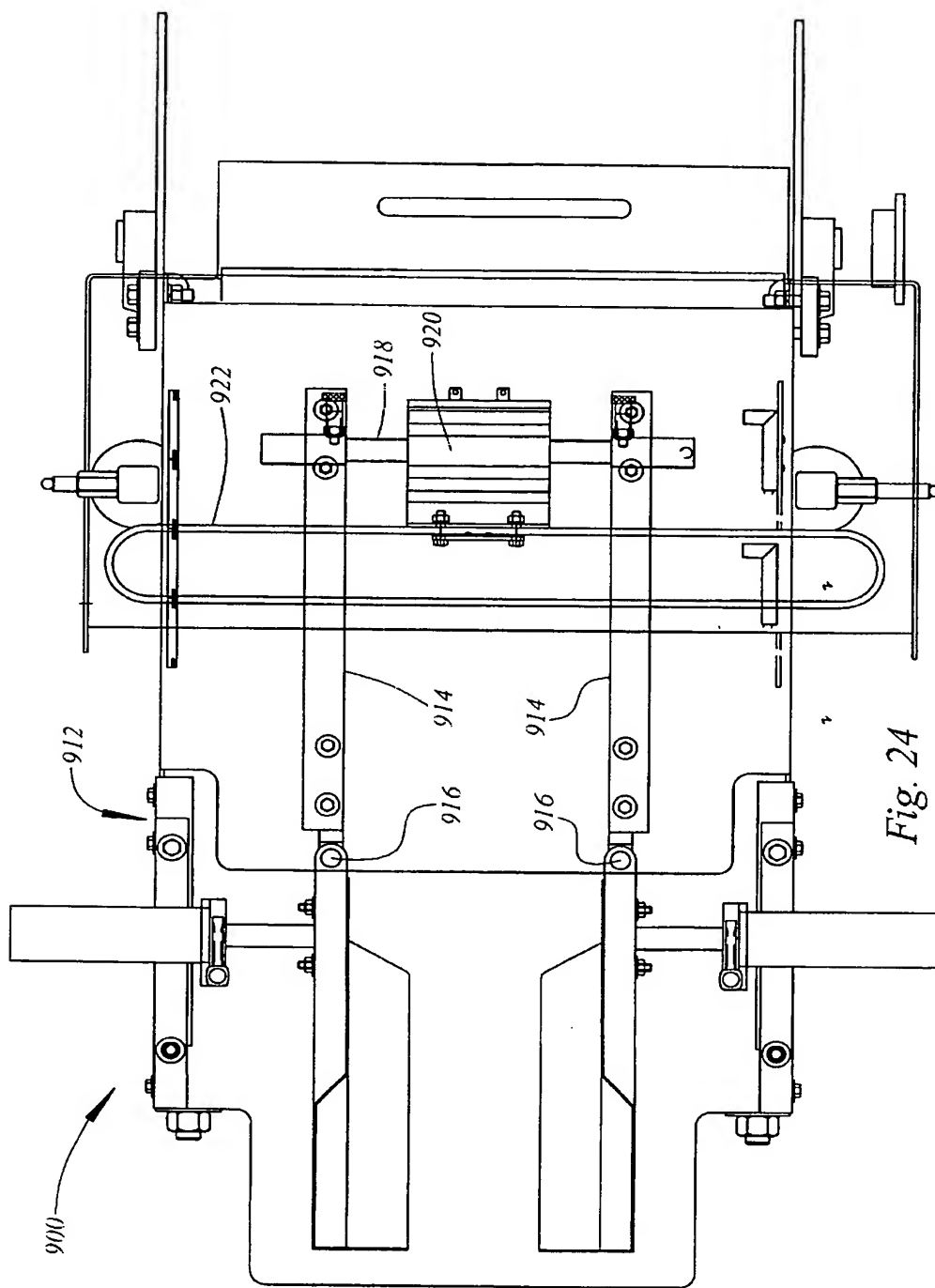


Fig. 24

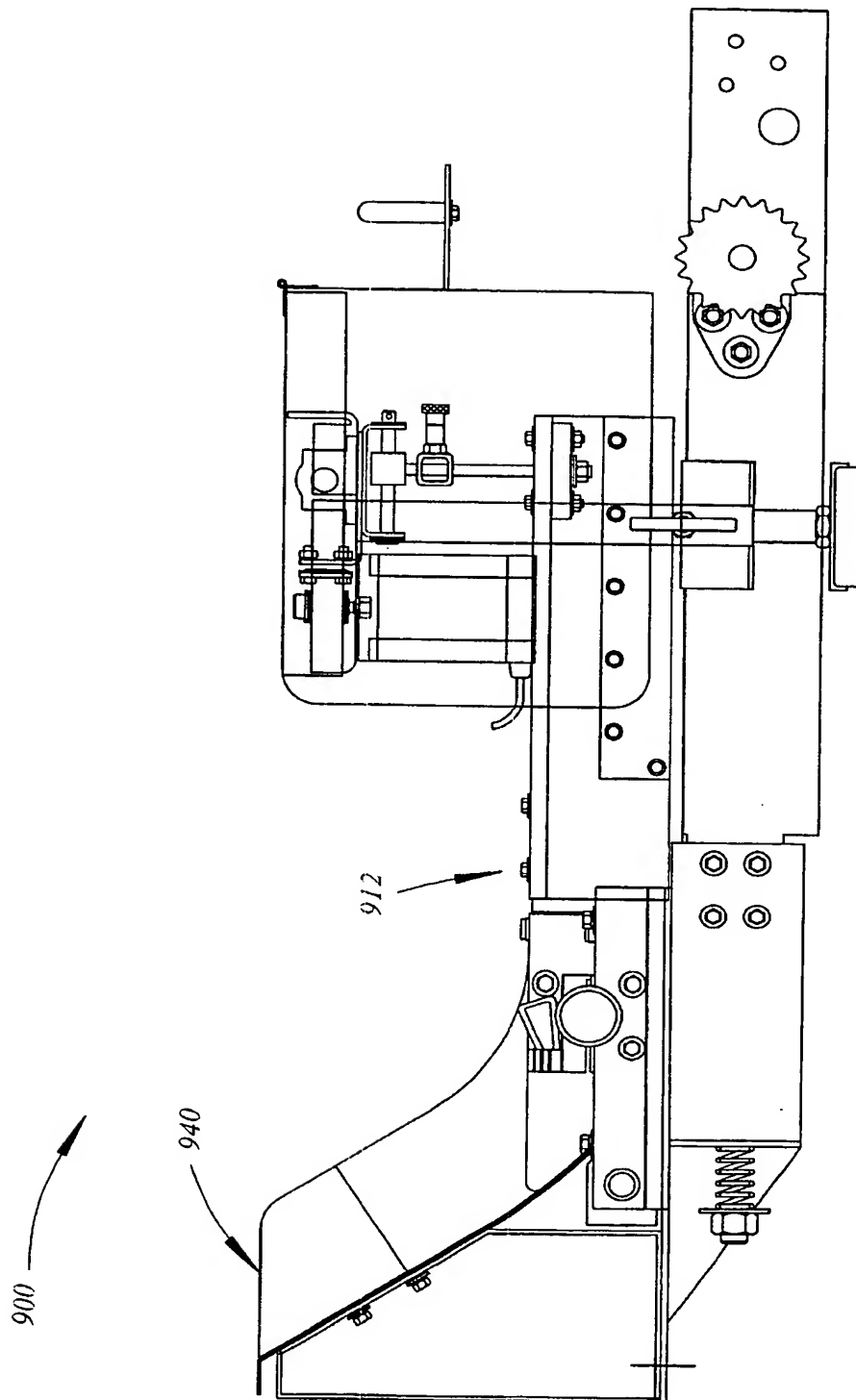


Fig. 25

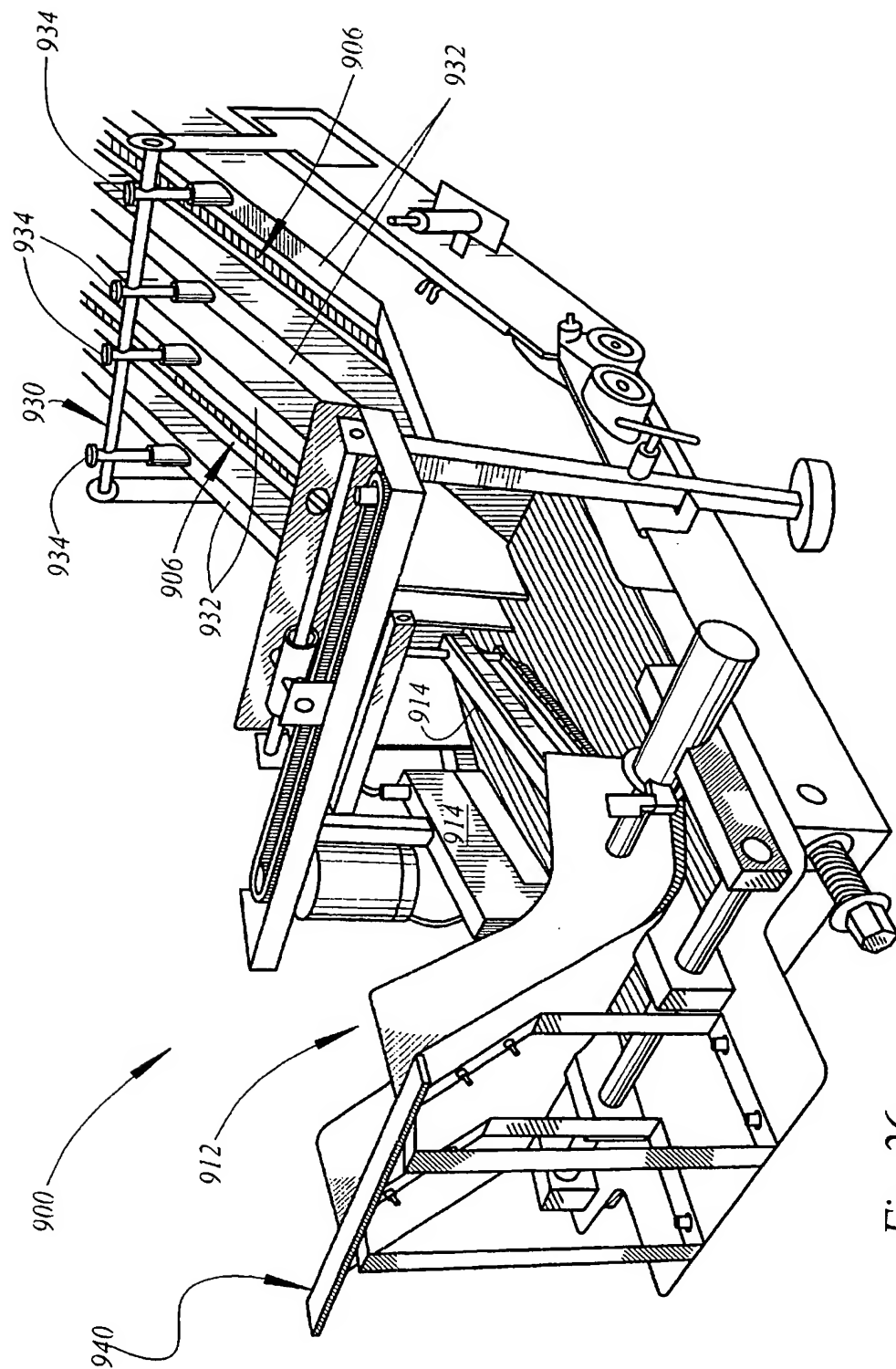


Fig. 26



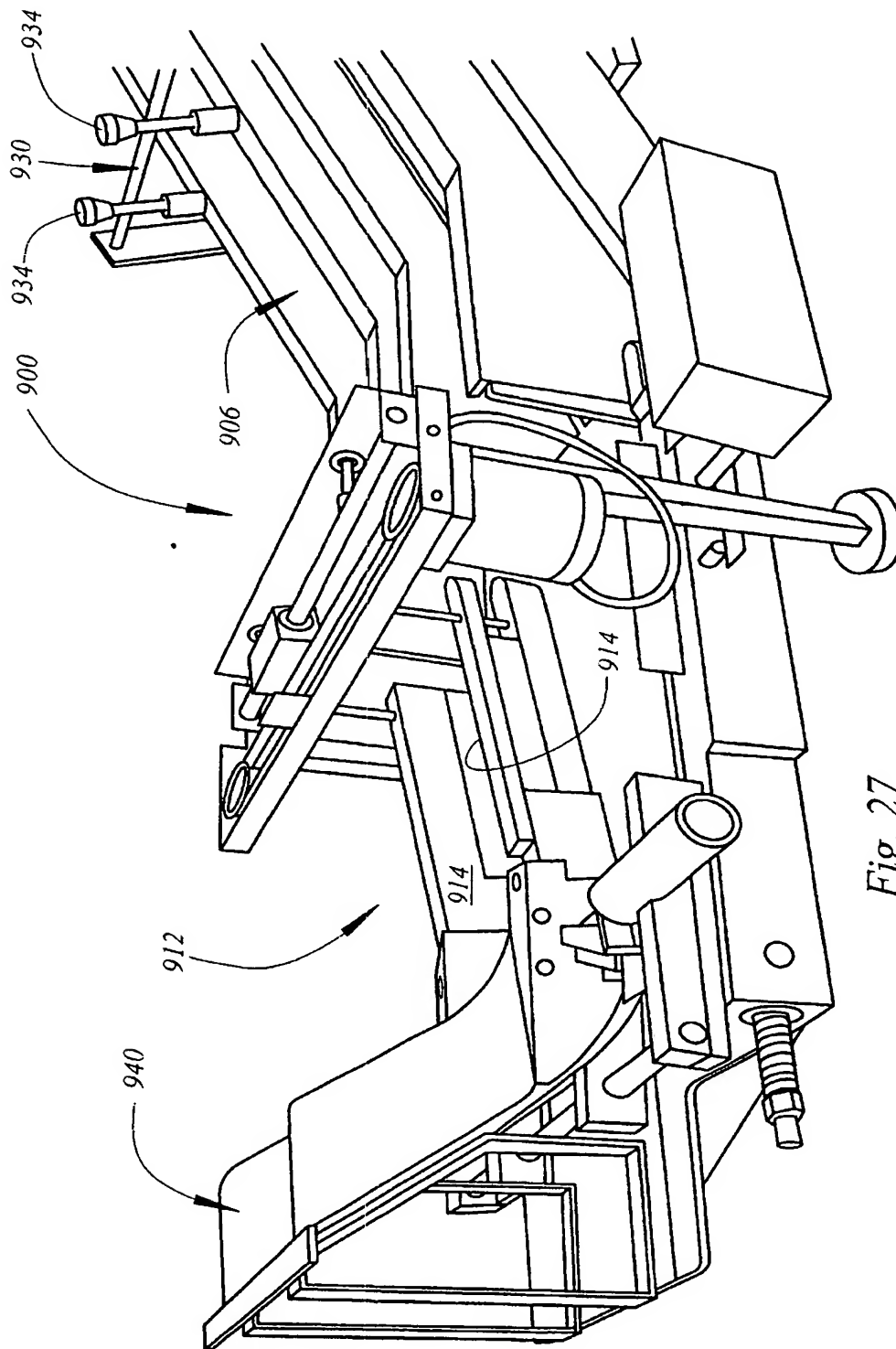
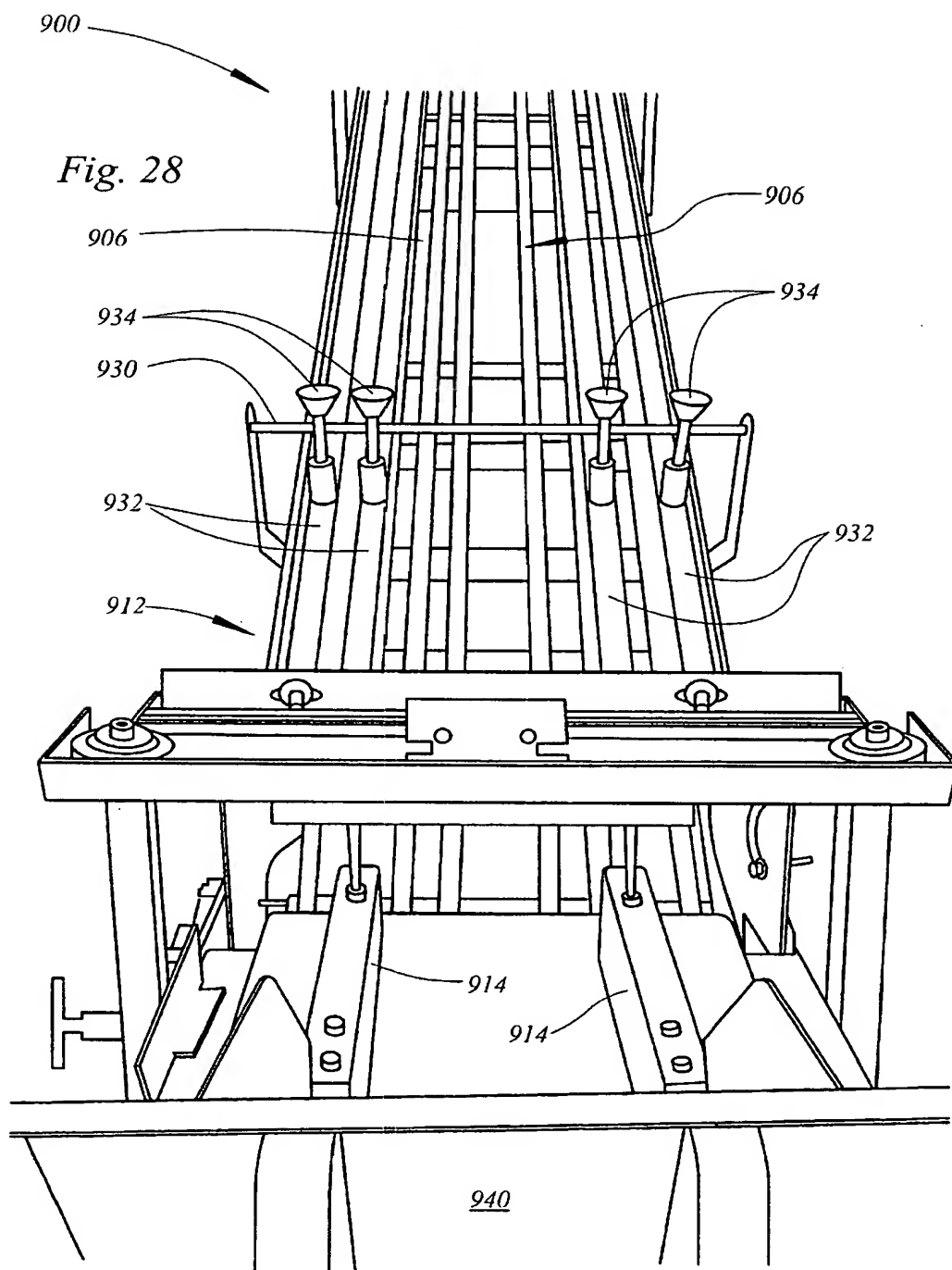


Fig. 27

BEST AVAILABLE COPY



BEST AVAILABLE COPY

